

Resonance Characterization and Frequency-divided Compensation Strategy for Heterogeneous Inverters-paralleled System

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Abstract—Currently, the dominant trend in new energy power supply systems is the heterogeneous inverters-paralleled system (HIPS), which is a combination of grid-following (GFL) and grid-forming (GFM) inverters. The dynamic characteristics of different inverters in HIPS and the differences between GFL and GFM inverters undoubtedly increase the difficulty of the stability analysis and coordinated control. This paper establishes an interactive admittance matrix model of HIPS, fully considers the interactive effects among different inverters, and explores the multi-dimensional resonance characteristics of HIPS by utilizing the modal analysis method. To achieve the coordinated control and oscillation suppression among different inverters, a frequency-divided compensation strategy is proposed, which divides the operation modes of HIPS into three categories, i.e., GFM, GFL, and hybrid modes. Specifically, the frequency division boundary is determined based on the resonance characteristics of GFL and GFM inverters, with the operation modes of HIPS being dynamically adjusted according to the harmonic power ratio. Finally, the simulation and experimental results demonstrate that the HIPS can flexibly adjust the operation modes to adapt to the complex conditions after adopting the frequency-divided compensation strategy and suppressing the oscillation frequency ratio to less than 2%, ensuring the safe and reliable operation of HIPS.

Index Terms—Grid-following (GFL) inverter, grid-forming (GFM) inverter, heterogeneous inverters-paralleled system (HIPS), frequency-divided compensation strategy.

NOMENCLATURE

ω_1	Grid angular frequency
φ_{i0}	Phase of fundamental current
\mathbf{A}, λ_i	Diagonal eigenvalue matrix and eigenvalue
θ_{v1}	Phase of initial voltage
θ_{PSL}	Phase angle of power synchronization loop

θ_{PLL}	(PSL) in grid-forming (GFM) inverter
$B_{\text{PLL-GFL}}$	Phase angle of phase-locked loop (PLL) in grid-following (GFL) inverter
$B_{\text{CL-GFL}}$	PLL bandwidth of GFL inverter
$B_{\text{VL-GFM}}$	Current loop bandwidth of GFL inverter
$B_{\text{CL-GFM}}$	Voltage loop bandwidth of GFM inverter
$C_p(s), C_n(s)$	Current loop bandwidth of GFM inverter
C_v, R_{vc}	Transfer functions of θ_{PSL} for positive- and negative-sequence voltage perturbations
$C_{v\text{max}}$	Virtual capacitance and resistance of $Z_c(s)$
d, q	The maximum virtual capacitance of $Z_c(s)$
E_r	Indices of d and q axes
f_{spwm}	Output voltage of PSL
f_s	Switching frequency
$G_d(s)$	Grid frequency
$H_i(s)$	Equivalent transfer function of control delay links
$H_{\text{PLL}}(s)$	Transfer function of current loop in GFL inverter
$H_{vv}(s)$	Transfer function of PLL
$H_{vc}(s)$	Transfer function of voltage loop in GFM inverter
I_0, V_0	Transfer function of current loop in GFM inverter
I_i	Transfer function of current loop in GFM inverter
I_f	Peak values of fundamental current and grid voltage
i_g, u_g	Equivalent output current of inverter i
i_{g1}, i_{g2}	Node current matrix at frequency f
i_{dq1}, i_{dq2}	Grid-connected current and voltage
i_{gh}^h, i_{gl}^h	Grid-connected currents of GFL and GFM inverters
i_{dq1}^{ref}	Grid-connected currents of GFL and GFM inverters in dq -axis
i_L	High- and low-frequency components of harmonic current
K_p	Reference value of i_{dq1}
$K_{\text{PLL}}, K_{\text{iPLL}}$	Converter output current
	Equivalent coefficient of modulation link
	Proportional and integral parameters of PLL

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K_{pi}, K_{ii}	Proportional and integral parameters of current loop in GFL inverter
K_p, K_q	Active and reactive droop coefficients
K_{vpi}, K_{vii}	Proportional and integral parameters of voltage loop in GFM inverter
K_{cpi}, K_{cii}	Proportional and integral parameters of current loop in GFM inverter
k_c	Compensation coefficient of GFL inverter
k_v	Compensation coefficient of GFM inverter
\mathbf{L}, \mathbf{T}	Left and right eigenvector matrices, $\mathbf{L} = \mathbf{T}^{-1}$
L_{11}, C_{f1}, L_{12}	Parameters of LCL filter for GFL inverter
L_{21}, C_{f2}, L_{22}	Parameters of LCL filter for GFM inverter
L_v, R_{vl}	Virtual inductance and resistance of $Z_L(s)$
L_{vmax}	The maximum virtual inductance of $Z_L(s)$
n	Number of inverters
P_{ref}, P_e	Rated and actual active power
Q_{ref}, Q_e	Rated and actual reactive power
R_g, L_g	Grid resistance and inductance
S_F, S_H, S_L	Total system, high-frequency, and low-frequency harmonic power
S_{GFL}, S_{GFM}	Rated capacities of GFL and GFM converters
$T_p(s), T_n(s)$	Transfer functions of θ_{PLL} for positive- and negative-sequence voltage perturbations
T_s	Sampling period
U_{ref}, ω_{ref}	Root mean square (RMS) values of line voltage and angular frequency
U_f	Node voltage matrix at frequency f
U_s	Node voltage matrix of heterogeneous inverters-paralleled system (HIPS)
u_{PCC}	Voltage of point of common coupling (PCC)
u_{dq}	Voltage of PCC in dq -axis
U_i	Equivalent output voltage of inverter i
U_{PCC}	Magnitude of U_{PCC}
x	Variable parameter for HIPS
\mathbf{Y}	Interactive admittance matrix of HIPS
\mathbf{Y}_f	Node admittance matrix at frequency f
Y_{ii}	Equivalent self-admittance of inverter i
$Y_{ij} (i \neq j)$	Admittance denoting interactive effect between inverters i and j
Y_{ig}	Admittance denoting interactive effect between inverter i and grid
$Y_{GFLp}(s), Y_{GFLn}(s)$	Positive- and negative-sequence output admittances of GFL inverter
$Y_{GFMp}(s), Y_{GFMn}(s)$	Positive- and negative-sequence output admittances of GFM inverter
Y_{si}	Equivalent output admittance of inverter i , representing $Y_{GFMp}(s)$ or $Y_{GFLp}(s)$
Z_g, Y_g	Grid equivalent impedance and admittance
$Z_L(s)$	Function of virtual resistance-inductance strategy
$Z_C(s)$	Function of virtual resistance-capacitance strategy

I. INTRODUCTION

THE grid-following (GFL) inverters serve as essential interfaces for integrating renewable energy sources into the power grid, thereby enhancing the utilization of renewable resources. However, the GFL inverters face notable limitations in supporting grid voltage and frequency [1], [2]. Consequently, the grid-forming (GFM) inverters have gained significant interest as a promising solution to regulating grid-connected inverters, particularly in systems with high renewable energy penetration [3], [4]. In such systems, the GFL and GFM inverters coexist for extended periods, forming a heterogeneous inverters-paralleled system (HIPS) [5].

HIPS combines the advantages of both GFL and GFM inverters, providing voltage and frequency supports along with rapid power responses. However, it also inherits the drawbacks associated with each type of inverter. For instance, GFL inverters are susceptible to high-frequency oscillations in weak grids [6], [7], while GFM inverters are prone to low-frequency oscillations in strong grids [8], [9]. Furthermore, the interactions among different types of inverters in the HIPS may lead to multi-dimensional oscillation issues that encompass both high and low frequencies. Therefore, in a heterogeneous system such as HIPS, which comprises various grid-connected inverters, the coordinated control among the inverters becomes crucial for optimizing the overall system performance.

Currently, numerous studies have been conducted on the small-signal stability of inverters-paralleled system. However, most of them focus on the equivalent model [10], instability mechanism [11], and analytical tools for single-type inverters-paralleled system [12]. In particular, the studies on GFL inverters-paralleled system mainly primarily emphasizes the phase-locked loop (PLL) control [13], and the studies on GFM inverters-paralleled system are mostly concentrated on the power synchronization loop (PSL) control, including droop control [14], virtual synchronization control [15], etc.

In addition to the aforementioned studies focusing on the characteristics of single-type inverters-paralleled systems, some researchers have conducted comparative analyses of these two types of inverters. In [16], the second-order simplified models for both GFL and GFM inverters have been established to analyze their grid-connected synchronous characteristics. However, the research on HIPS remains relatively scarce. Although an initial state-space matrix model of HIPS is developed to investigate the power penetration law in islanded systems [17], the state-space matrix modeling method involves too numerous state variables to be generalized to multi-type inverters-paralleled systems.

Apart from the time-domain state-space matrix modeling of HIPS in [17], the impedance modeling based on the frequency-domain impedance is more commonly applied in multi-type inverters-paralleled system. For instance, in [18], Norton equivalent circuit and nodal admittance matrix for GFL inverters-paralleled system are established, and the modal analysis is employed to qualitatively examine the influence of system parameters and control variables on stability. However, this approach only provides simplified modeling for GFL inverters-paralleled system, neglecting factors

such as PLL.

The coordinated control among different inverters can be categorized into power allocation and power quality management [19], [20]. The power allocation problem is particularly relevant in islanded systems, where a leader-follower consensus control is proposed in [6] to distribute power evenly between GFL and GFM inverters, with GFM inverter acting as the leader and GFL inverter as the follower. Although this control leverages the primary frequency modulation capabilities of GFL inverter, it reduces the energy utilization efficiency.

The power quality management involves the mitigation of harmonic issues, including high-frequency oscillations of GFL inverters-paralleled system in weak grids and low-frequency oscillations of GFM inverters-paralleled system in strong grids. To tackle these challenges, [21] categorizes the resonance types of GFL inverters-paralleled system into dynamic and static resonances and proposes a hybrid multi-resonance suppression strategy. However, this method is complicated in its calculations, requiring momentary adjustments to the suppression strategy. In [22], a voltage feed-forward phase compensation method is proposed for GFL inverters-paralleled system. However, it necessitates a trade-off between the harmonic attenuation and resonance destabilization. A conduction compensator is designed in [23] to enhance the damping performance of GFM inverters-paralleled system, thereby improving the dynamic performance under variations in power reference tracking and load disturbances. Nevertheless, the above studies addressing power quality are limited to single-type inverters and do not recognize that the actual power supply system is HIPS. Therefore, the coordinated control between these two types of inverters is crucial for optimizing the HIPS performance.

In summary, the existing research is limited to single-type inverters-paralleled system. The study on the HIPS stabilization mechanism needs to be sufficiently intensive, and the interactive mechanisms of different types of inverters need to be identified. Moreover, the corresponding coordinated con-

trol among different inverters requires improvement. In this regard, the specific contributions of this paper are as follows.

1) This paper establishes an interactive admittance matrix model of HIPS. This model introduces the self-admittance to represent the equivalent effect of the inverters themselves and mutual admittance to characterize the interactions among different inverters, thereby capturing the dynamic interactive characteristics of HIPS more accurately.

2) This paper investigates the multi-dimensional resonance characteristics of HIPS through modal analysis and parameter sensitivity analysis methods. It analyzes the dominant factors contributing to oscillatory destabilization under different operating conditions and examines the interactive mechanisms of GFL and GFM inverters.

3) This paper proposes a frequency-divided compensation strategy for HIPS. This strategy flexibly adjusts the operation mode according to the ratio of high-frequency harmonic power to low-frequency harmonic power, reduces the oscillation frequency ratio to less than 2%, and ensures the safe, reliable, and stable operation of HIPS.

This paper is organized as follows. The interactive admittance matrix model of HIPS is established in Section II. Section III explores the HIPS stability and identifies the dominant factors using modal analysis and parameter sensitivity analysis, and Section IV proposes a frequency-divided compensation strategy for the HIPS. Simulation and experimental results are given in Section V. Finally, Section VI draws conclusions.

II. INTERACTIVE ADMITTANCE MATRIX MODEL OF HIPS

The structure of HIPS is illustrated in Fig. 1, including the GFL inverter, GFM inverter, and the power grid. It can be observed that the GFL inverter employs a PLL to achieve synchronization, and the GFM inverter utilizes the PSL to achieve self-synchronization [24]. It is noteworthy that both GFL and GFM inverters are controlled based on dq -axis. SPWM is short for sinusoidal pulse-width modulation.

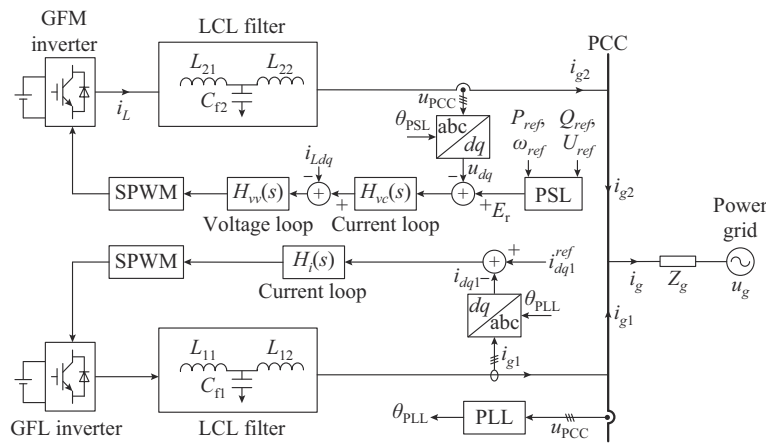


Fig. 1. Structure of HIPS.

A. Sequential Admittance Model of GFL Inverter

As illustrated in Fig. 1, the GFL inverter adopts the typical PLL based on the synchronized coordinate system and the current-loop control based on the dq coordinate system. The trans-

fer function of PLL, i. e., $H_{PLL}(s)$, can be expressed as (1), whose structure depicted in Appendix A Fig. A1 [25].

$$H_{PLL}(s) = \frac{K_{pPLL} + K_{iPLL}/s}{s} \quad (1)$$

The harmonic linearization method is capable of modeling the PLL and the current loop while also accounting for the influence of control delay. This paper draws upon the comprehensive derivation of the PLL-based sequence-impedance model for GFL inverters outlined in [26], hence the detailed derivation process is not reiterated here. The positive- and negative-sequence output admittances of GFL inverters, i.e., $Y_{\text{GFLp}}(s)$ and $Y_{\text{GFLn}}(s)$, respectively, are obtained and presented as:

$$\begin{cases} Y_{\text{GFLp}}(s) = \frac{Y_1(s) - K_p \left(H_i(s - j\omega_1) G_d(s) + \frac{V_0}{I_0 e^{j\varphi_0}} \right) T_p(s) I_0 e^{j\varphi_0}}{Y_2(s) + K_p H_i(s - j\omega_1) G_d(s)} \\ Y_{\text{GFLn}}(s) = \frac{Y_1(s) - K_p \left(H_i(s + j\omega_1) G_d(s) + \frac{V_0}{I_0 e^{-j\varphi_0}} \right) T_n(s) I_0 e^{-j\varphi_0}}{Y_2(s) + K_p H_i(s + j\omega_1) G_d(s)} \end{cases} \quad (2)$$

where $T_p(s)$ and $T_n(s)$ are detailed in Appendix B (B1); $Y_1(s)$ and $Y_2(s)$ are detailed in Appendix B (B2); and $G_d(s)$ is expressed as:

$$G_d(s) = \frac{1 - e^{-sT_s}}{sT_s} e^{-sT_s} \approx e^{-1.5sT_s} \quad (3)$$

The current loop adopts a proportional-integral (PI) controller, which is expressed as:

$$H_i(s) = K_{pi} + \frac{K_{ii}}{s} \quad (4)$$

B. Sequential Admittance Model of GFM Inverter

As illustrated in Fig. 1, the PSL of GFM inverter employs typical droop control for self-synchronization, with its detailed structure depicted in Appendix A Fig. A2. The transfer function representing the principle of droop control is expressed as [27]:

$$\begin{cases} \theta_{\text{PSL}}(s) = \frac{\omega_{\text{ref}} - K_p(P_e - P_{\text{ref}})}{s} \\ E_r(s) = U_{\text{ref}} - K_q(Q_e - Q_{\text{ref}}) \end{cases} \quad (5)$$

Since the inverters are connected to the PCC in parallel, the calculation of admittance in this circuit is simplified. Furthermore, the output characteristics of a GFM inverter can be expressed in terms of admittance. The positive- and negative-sequence output admittances of GFM inverters, i.e., $Y_{\text{GFMp}}(s)$ and $Y_{\text{GFMn}}(s)$, respectively, are derived based on [27] as:

$$\begin{cases} Y_{\text{GFMp}}(s) = \frac{Y_3(s) + K_p (H_{vc}(s - j\omega_1) H_{vv}(s - j\omega_1) - C_p(s) I_0 e^{-j\varphi_0}) G_d(s)}{Y_4(s) + K_p (H_{vc}(s - j\omega_1) + C_p(s) V_0) G_d(s)} \\ Y_{\text{GFMn}}(s) = \frac{Y_3(s) + K_p (H_{vc}(s + j\omega_1) H_{vv}(s + j\omega_1) + C_n(s) I_0 e^{-j\varphi_0}) G_d(s)}{Y_4(s) + K_p (H_{vc}(s + j\omega_1) + C_n(s) V_0) G_d(s)} \end{cases} \quad (6)$$

where $C_p(s)$, $C_n(s)$, $Y_3(s)$, and $Y_4(s)$ are elaborated in Appendix B (B3)-(B7). The voltage loop $H_{vv}(s)$ and current loop $H_{vc}(s)$ are defined as:

$$\begin{cases} H_{vv}(s) = K_{vpi} + \frac{K_{vii}}{s} \\ H_{vc}(s) = K_{cpi} + \frac{K_{cii}}{s} \end{cases} \quad (7)$$

C. Interactive Admittance Matrix Model of HIPS

To further investigate the interactive mechanism between GFL and GFM inverters, this paper establishes the interactive admittance matrix model of HIPS. As depicted in Fig. 2, this model introduces the mutual admittance $Y_{ij} (i \neq j)$ to represent the interactive effect between inverters and introduces the mutual admittance Y_{ig} to represent the interactive effect between inverters and the power grid. The interactive admittance matrix of HIPS can be derived as (8).

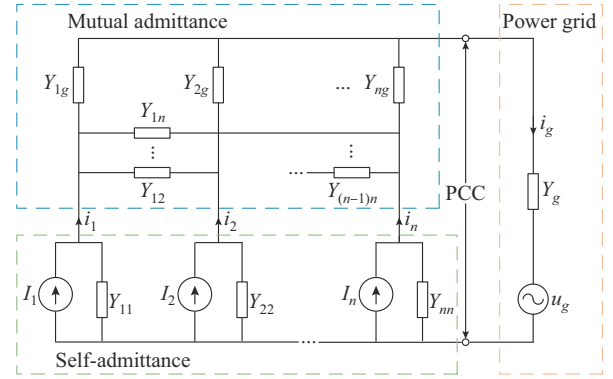


Fig. 2. Norton equivalent circuit for HIPS considering interactive effects.

$$\begin{bmatrix} I_1 \\ I_2 \\ \vdots \\ I_n \\ U_{\text{PCC}} Y_g \end{bmatrix} = \begin{bmatrix} Y_{11} & Y_{12} & \cdots & Y_{1n} & Y_{1g} \\ Y_{21} & Y_{22} & \cdots & Y_{2n} & Y_{2g} \\ \vdots & \vdots & \ddots & \vdots & \vdots \\ Y_{n1} & Y_{n2} & \cdots & Y_{nn} & Y_{ng} \\ Y_{g1} & Y_{g2} & \cdots & Y_{gn} & Y_g \end{bmatrix} \begin{bmatrix} U_1 \\ U_2 \\ \vdots \\ U_n \\ U_{\text{PCC}} \end{bmatrix} = \mathbf{Y} \mathbf{U}_s \quad (8)$$

where $Y_{gi} = Y_{ig}$.

Note that because the line inductor is in series with the grid-side filter inductor, it can be directly added to the grid-side filter inductor [28], which further facilitates the analysis of the HIPS stabilization mechanism. To facilitate theoretical calculations and expressions, the grid impedance is described in the admittance form Y_g . The nonzero elements are expressed as:

$$\begin{cases} Y_{ii} = Y_{si} // \sum_{j=1, j \neq i}^n Y_{sj} \\ Y_{ij, i \neq j} = - \left(Y_{si} // \sum_{j=1, j \neq i}^n Y_{sj} \right) \left(Y_{sj} / \sum_{j=1, j \neq i}^n Y_{sj} \right) \\ Y_{ig} = - \left(Y_{si} // \sum_{j=1, j \neq i}^n Y_{sj} \right) \left(Y_g / \sum_{j=1, j \neq i}^n Y_{sj} \right) \end{cases} \quad (9)$$

where the symbol “//” indicates a parallel connection. Notably, the positive-sequence admittance of the inverter provides more comprehensive information regarding its output characteristics [29]. Therefore, to simplify the arithmetic process, the positive-sequence admittance of the inverter will serve as an example for analyzing the resonance characteris-

tics of HIPS.

III. STABILITY ANALYSIS AND IDENTIFICATION OF DOMINANT FACTORS

This section employs the modal analysis and parameter sensitivity analysis to investigate the oscillation characteristics of HIPS and identify the dominant factors. Specifically, the HIPS comprises one GFL inverter and one GFM inverter, with their parameters detailed in Table I.

TABLE I
PARAMETERS OF HIPS

Parameter type	Symbol	Value
GFL inverter	S_{GFL}	10 kW
	$K_{p\text{PLL}}, K_{i\text{PLL}}$	1, 80
	K_{pi}, K_{ii}	2, 120
GFM inverter	S_{GFM}	10 kW
	K_p, K_q	$5 \times 10^{-3}, 2 \times 10^{-3}$
	K_{vpi}, K_{vii}	1, 20
	K_{cpi}, K_{cii}	5, 100
LCL filter	L_{11}, L_{21}	3.5 mH
	C_{f1}, C_{f2}	30 μF
	L_{12}, L_{22}	0.33 mH
Runtime environment	U_{PCC}, f_s	380 V, 50 Hz
	R_g, L_g	0.2 Ω , 0.5-20 mH
	T_s	1×10^{-5} s
	f_{spwm}	10 kHz

A. Modal Analysis and Parameter Sensitivity Analysis

The modal analysis method can be described as follows [18]. In a multi-node system, the harmonic resonance typically occurs when the network node admittance matrix \mathbf{Y} contains very small values. In extreme cases, the matrix \mathbf{Y} approaches singularity, resulting in significantly large values in \mathbf{Y}^{-1} . Assuming the resonance occurs at a frequency f , the nodal voltage-current equation of HIPS can be expressed as:

$$\mathbf{U}_f = \mathbf{Y}_f^{-1} \mathbf{I}_f \quad (10)$$

\mathbf{Y}_f can be decomposed into the following form:

$$\mathbf{Y}_f = \mathbf{L} \mathbf{A} \mathbf{T} \quad (11)$$

where $\mathbf{A} = \text{diag}(\lambda_1, \lambda_2, \dots, \lambda_n)$. Notably, the eigenvalues derived from the inverse of the node admittance matrix have impedance characteristics, which are defined as modal impedance (MI). MI denotes the ratio of voltage to current for a particular oscillatory mode of HIPS, and a large MI value indicates that the HIPS responds more dramatically to current variations in that mode, which may lead to system destabilization.

The modal analysis method involves solving the eigenvalues of the node admittance matrix \mathbf{Y} across the entire frequency spectrum and subsequently plotting the distribution of these eigenvalues. This visualization aids in the analysis of the oscillation characteristics of HIPS.

Furthermore, to identify the dominant factors of HIPS, the parameter sensitivity analysis is essential. A commonly employed approach for conducting such analysis involves computing the eigenvalue matrix of the partial derivatives of the

relevant parameters [30]. This paper adopts the conventional normalization method for parameter sensitivities, enabling their expression in percentages to better elucidate the degree of their respective influences. The normalized sensitivities can be described as:

$$\frac{\partial |\lambda|}{\partial x} = \frac{\partial |\lambda_i| / |\lambda_i|}{\partial x / x} = \frac{\partial |\lambda_i|}{\partial x} \frac{x}{|\lambda_i|} \quad (12)$$

Figure 3 depicts the flowchart of the modal analysis and parameter sensitivity analysis. Specifically, during the modal analysis, this paper defines the frequency range $[f_1, f_2] = [1, 104] \text{ Hz}$ and the frequency step $\Delta f = 5 \text{ Hz}$ to analyze the accuracy requirements [31]. During the parameter sensitivity analysis, this paper gives the short-circuit ratio (SCR) range as $[M_1, M_2] = [2, 6]$ with a step of $\Delta M = 0.5$.

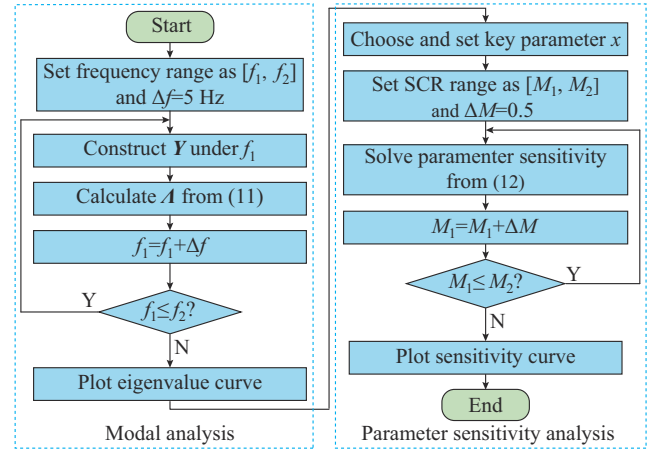


Fig. 3. Flowchart of modal analysis and parameter sensitivity analysis.

It is noteworthy that the quantitative analysis of the sensitivity of critical parameters can yield positive or negative results, which can be interpreted as follows. A positive sensitivity indicates a positive correlation, suggesting that an increase in the parameter leads to a rise in its equivalent negative impedance, thus increasing the risk of system destabilization. Conversely, a negative sensitivity indicates the opposite trend.

B. Stability Estimation of HIPS

The HIPS, consisting of GFL and GFM inverters, integrates the advantages of these two inverters and also retains the inherent instability risks associated with each inverter under varying SCR conditions. Consequently, this paper employs the modal analysis to investigate the oscillation characteristics and conducts parameter sensitivity analysis based on the interactive admittance matrix model of HIPS.

Figure 4 illustrates the stability analysis results of HIPS under various SCRs, revealing three resonance peaks identified as States I, II, and III, respectively. When combined with the parameter sensitivity analysis results in Fig. 5, several observations emerge.

1) State I: as SCR increases, the risk of GFM inverter destabilization in the low-frequency band intensifies. The parameter sensitivity analysis indicates that the primary cause of destabilization in the low-frequency band is the destabilization of the voltage loop due to the minimum grid imped-

ance. Notably, according to (13), a decrease in inductive reactance value X_L leads to a rise in the instantaneous reactive power Q_L with a constant voltage U_{PCC} . Consequently, the PSL experiences significant power fluctuations, triggering detuning destabilization, which further results in voltage loop destabilization.

$$Q_L = U_{PCC}^2 / X_L \quad (13)$$

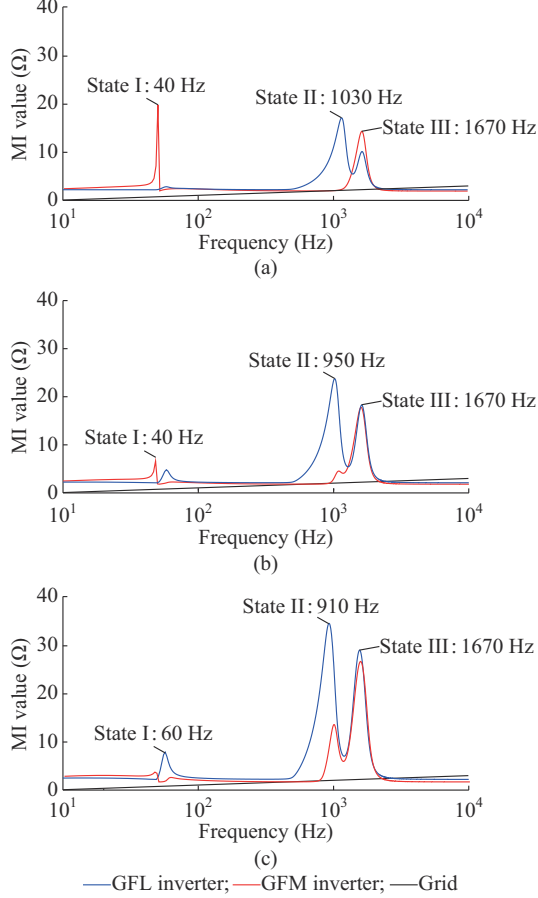


Fig. 4. Modal analysis of HIPS under different SCRs. (a) $SCR=6$. (b) $SCR=4$. (c) $SCR=2$.

2) State II: an increase in grid impedance will exacerbate the coupling effect between the HIPS and the grid impedance, leading to resonance instability rapidly occurring in the mid- and high-frequency bands. The parameter sensitivity analysis reveals that the dominant factor determining the occurrence of resonance instability in this scenario is the bandwidth of the current loops of both types of inverters. However, the dominant factor contributing to resonance instability in this instance is the current loop of GFL inverter, attributing to the clamping effect of the voltage loop of GFM inverter.

3) State III: this resonant instability point is caused by the resonance of LCL filter in the inverter, whose resonance frequency f_{LCL} is calculated as (14). The coupling between the LCL filter and grid impedance intensifies as grid impedance increases.

$$f_{LCL} = \frac{1}{2\pi} \sqrt{(L_{11} + L_{12}) / (L_{11} L_{12} C_{f1})} \quad (14)$$

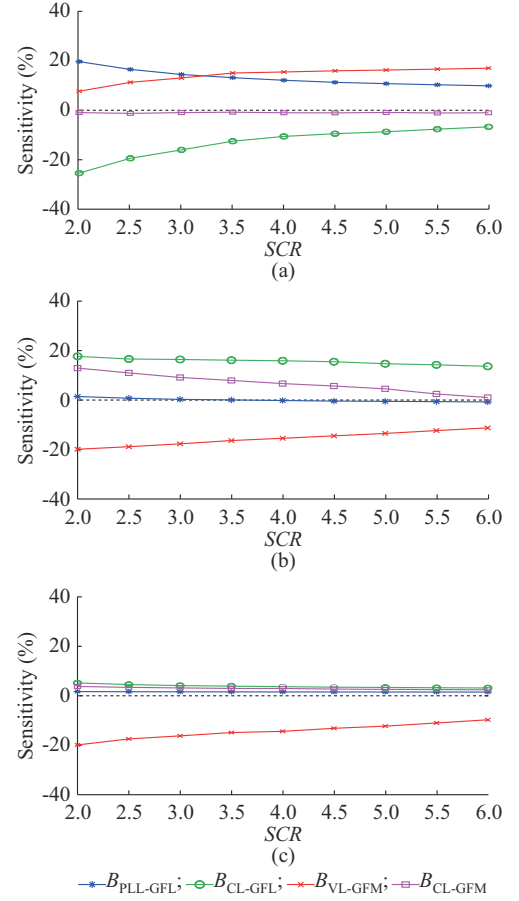


Fig. 5. Parameter sensitivity analysis of HIPS under different states. (a) State I. (b) State II. (c) State III.

In summary, the HIPS is susceptible to high-frequency resonance instability under weak grid conditions, which is essentially caused by the increased coupling between the current loop of GFL inverter and the grid impedance. Conversely, the HIPS is prone to low-frequency resonance instability under strong grid conditions, primarily stemming from the small grid impedance that destabilizes the voltage loop of GFM inverter.

IV. FREQUENCY-DIVIDED COMPENSATION STRATEGY

Considering the multi-dimensional resonance characteristics of HIPS, it is evident that GFL inverter primarily drives high-frequency resonance, while GFM inverter is responsible for low-frequency oscillations, categorized as sub-synchronous oscillations [32]. The boundary for frequency division is designated as 200 Hz, which is not strictly defined and resulted from weighing the output characteristics of HIPS.

As depicted in Fig. 6, firstly, the harmonic components i_g^h are extracted with the second-order generalized integrator (SOGI) and divided by a low-pass filter (LPF), obtaining i_{gh}^h and i_{gl}^h . Secondly, the high-frequency harmonic power (S_H) and low-frequency harmonic power (S_L) are calculated through power analysis. Finally, the compensation coefficients of these two inverters are assigned by the compensation coefficient command in (15).

$$\begin{cases} k_v=0, k_c=1 & |S_H| \geq 4\%|S_F|, |S_L| \leq 1\%|S_F| \\ k_c, k_v \in (0, 1) & |S_H| \geq 1\%|S_F|, |S_L| \geq 1\%|S_F|, |S_H| + |S_L| \geq 5\%|S_F| \\ k_c=0, k_v=1 & |S_L| \geq 4\%|S_F|, |S_H| \leq 1\%|S_F| \end{cases} \quad (15)$$

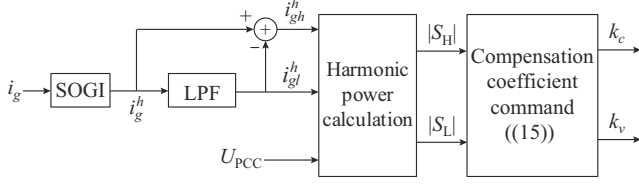


Fig. 6. Frequency-divided complementary control block diagram of HIPS.

Notably, according to the inverter grid-connected guidelines, the harmonic content of the grid-connected current must not exceed 5%. Therefore, when designing the operation mode boundary, the upper limit of harmonic content cannot exceed 5% of the total power, i. e., $|S_L| + |S_H| \leq 5\%|S_F|$. Additionally, to achieve the reasonable control and

harmonic suppression effect, the lower limit of single-type harmonics is bounded to less 1% of the total power, i. e., $|S_L| \leq 1\%|S_F|$ and $|S_H| \leq 1\%|S_F|$, as illustrated in Fig. 7. Corresponding to (15), Fig. 7 categorizes the operation modes into four types: GFM, hybrid, GFL, and stabilized modes. The stabilized mode remains unchanged, as it does not require oscillation suppression.

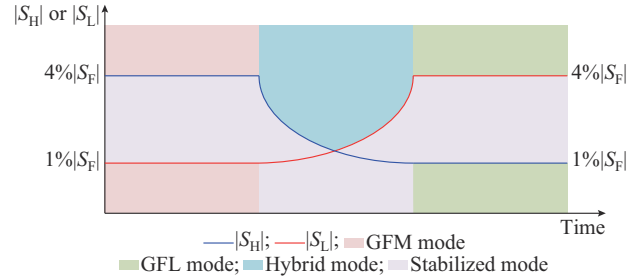


Fig. 7. Different operation modes of HIPS.

Figure 8 illustrates the specific implementation of the frequency-divided complementary strategy. The implementation steps of the specific mode are as follows.

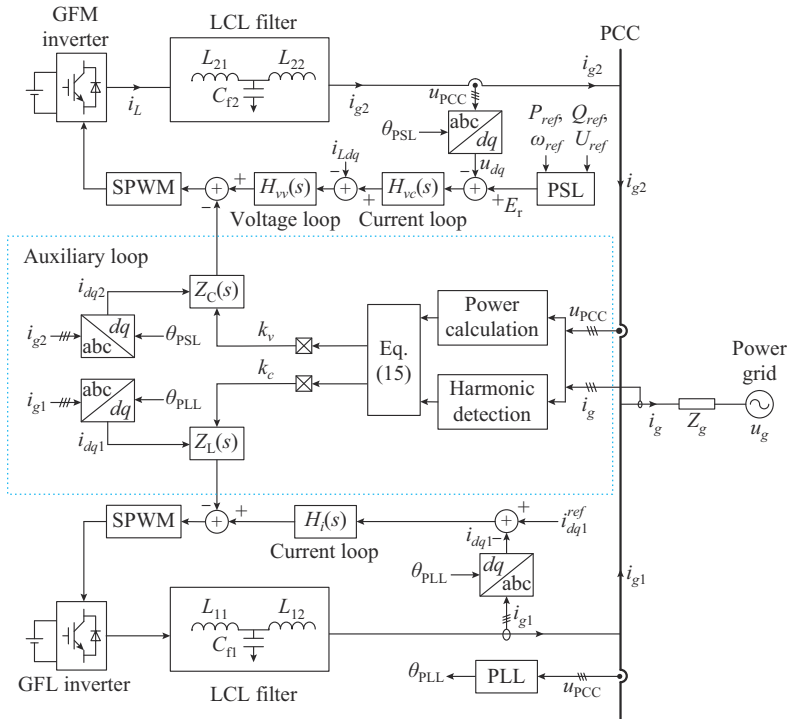


Fig. 8. Specific implementation of frequency-divided compensation strategy.

1) To address the high-frequency resonance instability in weak grids, an auxiliary loop is incorporated into the dominant GFL inverter. This approach involves introducing the virtual resistance-inductance strategy to address high-frequency resonance without altering the GFM inverter. This mode is referred to as the GFM mode, encompassing both the GFM inverter and the compensated GFL inverter.

2) To address the low-frequency oscillation instability in strong grids, an auxiliary loop is introduced for the dominant GFM inverter. This approach involves introducing the virtual resistance-capacitance strategy to suppress low-frequency

resonance while maintaining the GFL inverter unchanged. This mode is referred to as the GFL mode, comprising both the GFL inverter and the compensated GFM inverter.

3) When both high- and low-frequency resonances are present, the HIPS is classified as a hybrid model. In this configuration, both inverters are compensated by implementing auxiliary loops to resolve grid quality issues. The compensation factors for both inverters can be evenly allocated according to the levels of high- and low-frequency harmonic power.

Figure 9 illustrates the flowchart of the frequency-divided

compensation strategy for HIPS, which utilizes harmonic power to dynamically adjust operation mode.

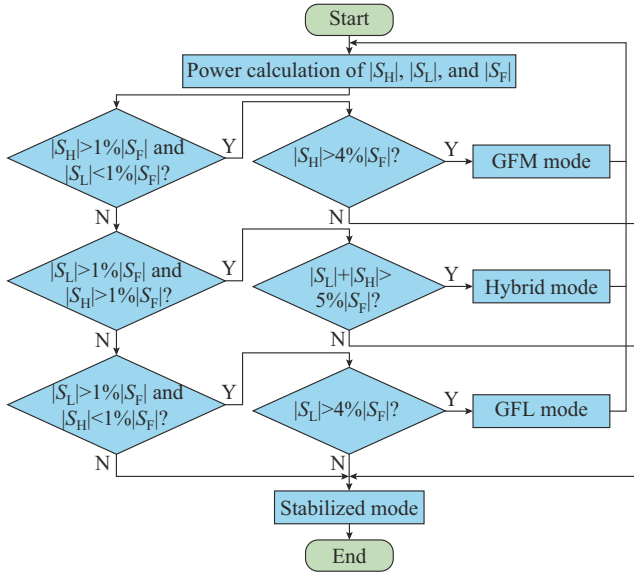


Fig. 9. Flowchart of frequency-divided compensation strategy for HIPS.

A. GFM Mode

In power systems, a lower equivalent input conductance improves the resistance to load variations, thereby enhancing the overall system stability. Given that the inductance has the property of passing low frequencies while blocking high frequencies, the increase of the inductance value in the power system can effectively reduce the equivalent input conductance, thereby improving anti-interference capabilities and stability.

Additionally, the research indicates that incorporating virtual inductance along with virtual resistance, to a certain extent, eliminates the negative damping effect of either virtual inductance or virtual resistance alone and improves the adjustment range of both values [33]. Thus, adding the virtual inductance to the control loop is undoubtedly the most economically viable choice.

Define the function of virtual resistance-inductance strategy as $Z_L(s)$:

$$Z_L(s) = R_{vl} + sL_v \quad (16)$$

As illustrated in Fig. 8, after incorporating $Z_L(s)$ to the GFL control loop, the equivalent positive- and negative-sequence admittances of GFL inverter can be expressed as:

$$\begin{cases} Y'_{GFLp}(s) = \frac{Y_1(s) - K_p \left(H_i(s - j\omega_1) G_d(s) + \frac{V_0}{I_0 e^{j\varphi_0}} - Z_L(s) \right) T_p(s) I_0 e^{j\varphi_0}}{Y_2(s) + K_p H_i(s - j\omega_1) G_d(s)} \\ Y'_{GFLn}(s) = \frac{Y_1(s) - K_p \left(H_i(s + j\omega_1) G_d(s) + \frac{V_0}{I_0 e^{-j\varphi_0}} - Z_L(s) \right) T_n(s) I_0 e^{-j\varphi_0}}{Y_2(s) + K_p H_i(s + j\omega_1) G_d(s)} \end{cases} \quad (17)$$

It is noticeable from Fig. 10 that as L_v increases, the resonance in the high-frequency range is effectively dampened; however, the risk of resonance in the low-frequency range escalates.

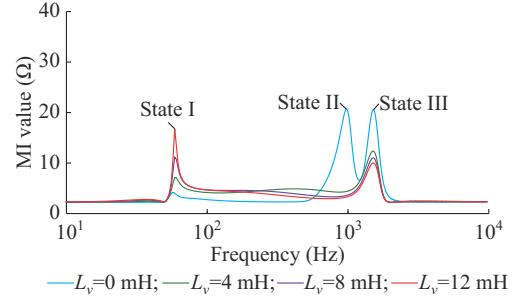


Fig. 10. Effect of different L_v values on stability of GFL inverter.

From the perspective of physical characteristics, since $Z_L(s)$ is implemented through the negative feedback link of grid current, the virtual inductance L_v is added to diminish the influence of grid inductance L_g . However, excessively large values of L_v may lead to system oscillations in the low-frequency range. Therefore, the value of L_v must be constrained. This paper illustrates the constraint curves of L_v with R_{vl} ($SCR=2$) and L_g ($R_{vl}=0 \Omega$), as shown in Fig. 11.

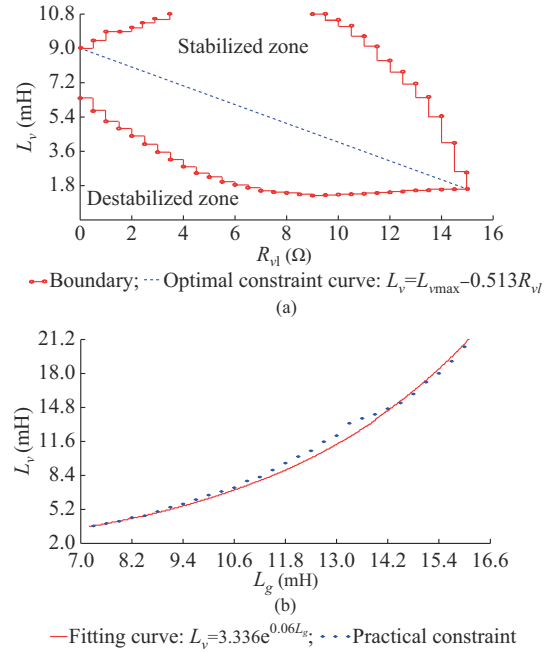


Fig. 11. Constraint curves of L_v with R_{vl} and L_g . (a) With R_{vl} ($SCR=2$). (b) With L_g ($R_{vl}=0 \Omega$).

By analyzing Fig. 11(a) and (b), the constraint of L_v with R_{vl} and L_g can be expressed as (18). Notably, the flowchart for drawing the constraint is illustrated in Supplementary Material A Fig. S1.

$$L_v = L_{vmax} - 0.513R_{vl} \approx 3.3e^{0.06L_g} - 0.513R_{vl} \quad (18)$$

Furthermore, the value of R_{vl} can be determined based on the relationship between the high-frequency harmonic power and the grid-connected current, as shown in (19).

$$R_{vi} = \frac{|S_H|}{1.5i_{gd}^2} \quad (19)$$

In summary, the value of $Z_L(s)$ implemented in the GFM mode can be expressed as (20). This strategy dynamically adjusts the values of virtual inductance and resistance based on grid impedance and high-frequency harmonic power, demonstrating the robust dynamic performance and environmental adaptability.

$$Z_L(s) = \frac{|S_H|}{1.5i_{gd}^2} + s \left(3.3e^{0.06L_g} - 0.5 \frac{|S_H|}{1.5i_{gd}^2} \right) \quad (20)$$

B. GFL Mode

The GFL mode is designed to suppress low-frequency oscillations in the HIPS. Considering that capacitance has the characteristic of passing high frequencies while blocking low frequencies, along with its economic benefits, an auxiliary loop with virtual capacitances is constructed for the GFM inverter to suppress low-frequency oscillations. Similarly, the function of virtual resistance-capacitance strategy $Z_C(s)$ can be expressed as:

$$Z_C(s) = C_v^{-1} s^{-1} + R_{vc} \quad (21)$$

With the incorporation of $Z_C(s)$ in the control loop of GFM inverter, the equivalent positive- and negative-sequence conductances of GFM inverter can be expressed as:

$$\begin{cases} Y'_{GFMp}(s) = \frac{Y_3(s)}{Y_4(s) + K_p(H_{vc}(s-j\omega_1) + C_p(s)V_0)G_d(s)} + \\ \frac{K_p(H_{vc}(s-j\omega_1)H_{vv}(s-j\omega_1) - C_p(s)I_0e^{-j\theta_0} - Z_C(s))G_d(s)}{Y_4(s) + K_p(H_{vc}(s-j\omega_1) + C_p(s)V_0)G_d(s)} \\ Y'_{GFMn}(s) = \frac{Y_3(s)}{Y_4(s) + K_p(H_{vc}(s+j\omega_1) + C_n(s)V_0)G_d(s)} + \\ \frac{K_p(H_{vc}(s+j\omega_1)H_{vv}(s+j\omega_1) + C_n(s)I_0e^{-j\theta_0} - Z_C(s))G_d(s)}{Y_4(s) + K_p(H_{vc}(s+j\omega_1) + C_n(s)V_0)G_d(s)} \end{cases} \quad (22)$$

From the analysis of physical characteristics, it is evident that $Z_C(s)$ is obtained through the current feedback link. The addition of virtual capacitance C_v effectively reduces the capacitive negative damping of GFM inverter in the low-frequency range. However, excessively large values of capacitance can easily trigger high-frequency oscillations due to the inherent physical characteristics of capacitance, as illustrated in Fig. 12.

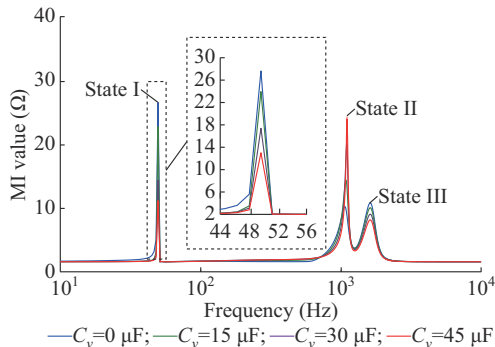


Fig. 12. Effect of different C_v values on stability of GFM inverter.

Consistent with the above analytical process, this paper examines the constraints of virtual capacitance C_v with R_{vc} and L_g , as shown in Fig. 13.

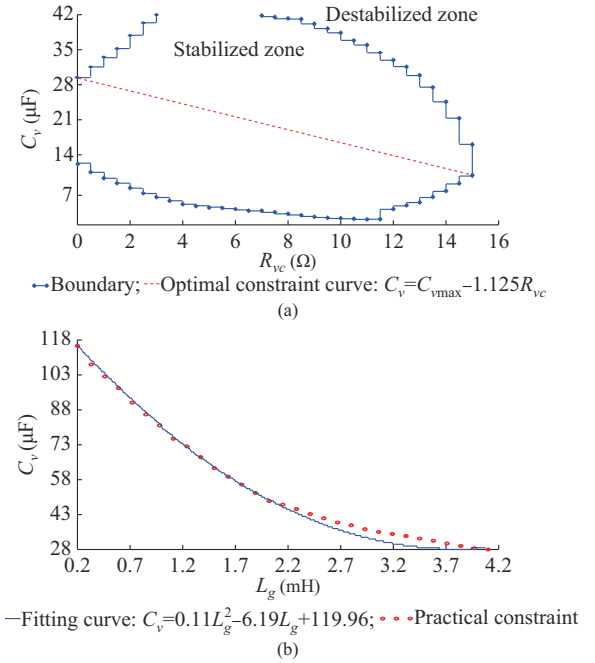


Fig. 13. Constraint curves of C_v with R_{vc} and L_g . (a) With R_{vc} ($SCR=6$). (b) With L_g ($R_{vc}=0 \Omega$).

$$C_v = C_{vmax} - 1.125R_{vc} \approx 0.11L_g^2 - 6.19L_g + 119.96 - 1.125R_{vc} \quad (23)$$

The value of R_{vc} is determined as:

$$R_{vc} = \frac{|S_L|}{1.5i_{gd}^2} \quad (24)$$

In summary, $Z_C(s)$ in GFL mode can be expressed as:

$$Z_C(s) = \frac{|S_L|}{1.5i_{gd}^2} + \left(0.11L_g^2 - 6.19L_g + 119.96 - 1.1 \frac{|S_L|}{1.5i_{gd}^2} \right)^{-1} s^{-1} \quad (25)$$

C. Hybrid Model

In the event of simultaneous high- and low-frequency oscillations in the HIPS, it is necessary to compensate both the GFL and GFM inverters. Specifically, the compensation coefficients of both inverters can be determined based on the high- and low-frequency harmonic contents. However, accurately measuring these harmonic contents in practical applications poses significant challenges. Therefore, this paper adjusts the compensation coefficients based on high- and low-frequency harmonic power, as illustrated in Fig. 14.

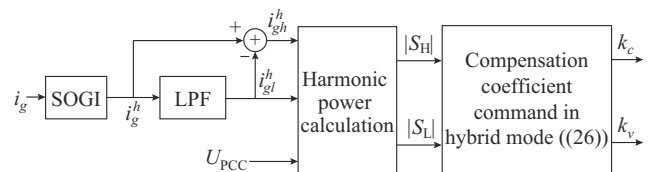


Fig. 14. Distribution of compensation coefficient in hybrid mode.

The compensation coefficients of HIPS for different inverters in hybrid mode are standardized, as shown in (26). When the HIPS exhibits a greater high-frequency harmonic content than low-frequency one, the corresponding compensation coefficient k_c for GFL inverter is higher than k_v for GFM inverter.

$$\begin{cases} |S_H| \geq 1\% |S_F| \\ |S_L| \geq 1\% |S_F| \\ |S_H| + |S_L| \geq 5\% |S_F| \\ |S_H| : |S_L| = k_c : k_v \\ k_c + k_v = 1 \end{cases} \quad (26)$$

In summary, this approach adjusts the operation modes based on system frequency detection and harmonic power calculations. This approach ensures that the HIPS demonstrates the GFL characteristics under strong grid conditions and GFM characteristics under weak grid conditions.

V. SIMULATION AND EXPERIMENT VERIFICATIONS

This paper builds an HIPS simulation model and an experimental platform to verify the dominant factors contributing to resonant instability and the effectiveness of frequency-divided compensation strategy for HIPS. The HIPS is constructed based on MATLAB/Simulink, utilizing the physical parameters listed in Table I. Notably, the experimental validation results for the stability domain and sensitivity of key parameters are presented in Supplementary Material A Section B. Furthermore, this paper verifies the accuracy of the HIPS model through the nodal current injection method, as elaborated in Supplementary Material A Section C.

The experimental platform comprises two DC sources, two inverters, two LCL filters, an inductance, a transformer, and a host computer, as shown in Fig. 15. Two inverters are paralleled at the PCC and connected to the power grid via the transformer. Specifically, each converter includes a power module, a sampling module, a communication module, and a control module.

The high-frequency, low-frequency, and hybrid oscillations in HIPS are influenced by control parameters and operation conditions, which are not fixed. Therefore, the control parameters of HIPS and the operation conditions presented in Table II are used to assess the effectiveness of the frequency-divided compensation strategy.

Figure 16(a) illustrates the output currents i_a , i_b , and i_c of HIPS and the FFT analysis results both before and after the implementation of GFL mode in response to low-frequency instability under Case I. Notably, through the integration of the virtual resistance-capacitance into the GFM inverter, the oscillations of HIPS at 40 Hz are reduced from 7.7% to 1.2% after the transition to the GFL mode, demonstrating significant suppression of the oscillations. Similarly, it is observed from Fig. 16(b) that the oscillations at 910 Hz and 1670 Hz are significantly suppressed to less than 0.3% following the transition to the GFM mode. Figure 16(c) illustrates that the hybrid oscillation frequencies include 40 Hz, 950 Hz, and 1670 Hz, all of which are suppressed to less than 0.5% following the transition to the hybrid mode, fur-

ther illustrating the effectiveness of the frequency-divided compensation strategy.

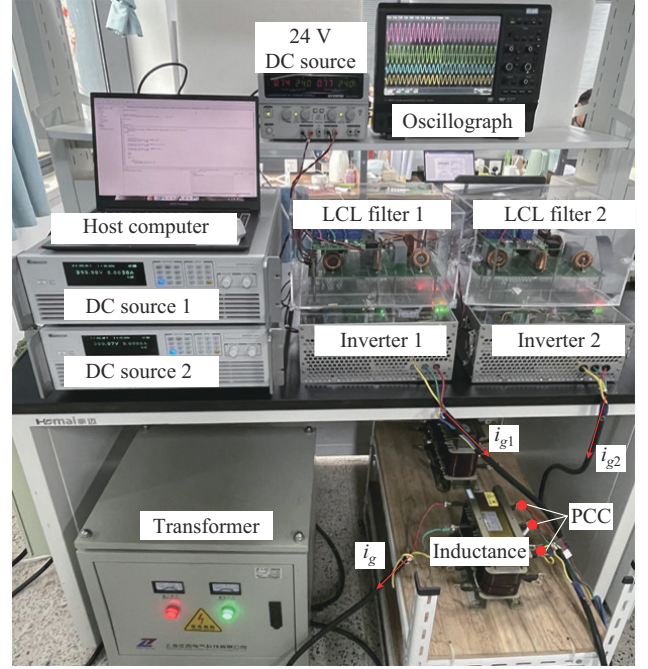


Fig. 15. Layout of experimental platform.

TABLE II
CONTROL PARAMETERS OF HIPS AND OPERATION CONDITIONS

Case	GFL inverter		GFM inverter		SCR
	$B_{PLL-GFL}$ (Hz)	B_{CL-GFL} (Hz)	B_{VL-GFM} (Hz)	B_{CL-GFM} (Hz)	
I	50	10	80	200	6
II	50	100	40	200	2
III	50	10	40	200	4

Figure 17(a) presents the comparison results of the output currents of HIPS and voltages at PCC accessing the GFL mode when low-frequency instability occurs under Case I. It can be noticed that the oscillation frequency content at 35 Hz is reduced from 5.8% to 0.7%. Additionally, Fig. 16(a) indicates that the HIPS oscillates at 40 Hz, while the experimental result shows at 35 Hz. Although this slight discrepancy exists, it does not affect the effectiveness of the strategy.

It is observed from Fig. 17(b) that the oscillation frequency content at 895 Hz decreases from 8.2% to 1.3%, and that at 1670 Hz decreases from 3.1% to 0.4%. Figure 17(c) demonstrates that the main oscillation frequency contents of 40 Hz, 930 Hz, and 1670 Hz are all suppressed to be less than 1%, which reveals the hybrid mode can effectively inhibit the multi-dimensional oscillation problem in the HIPS.

The combination of the above analyses and experimental validations confirms that the frequency-divided compensation strategy not only considers the actual operation conditions of the power grid, but also adeptly applies various modes to address the complex instability challenges, reducing the corresponding HIPS oscillation frequency to below 2%.

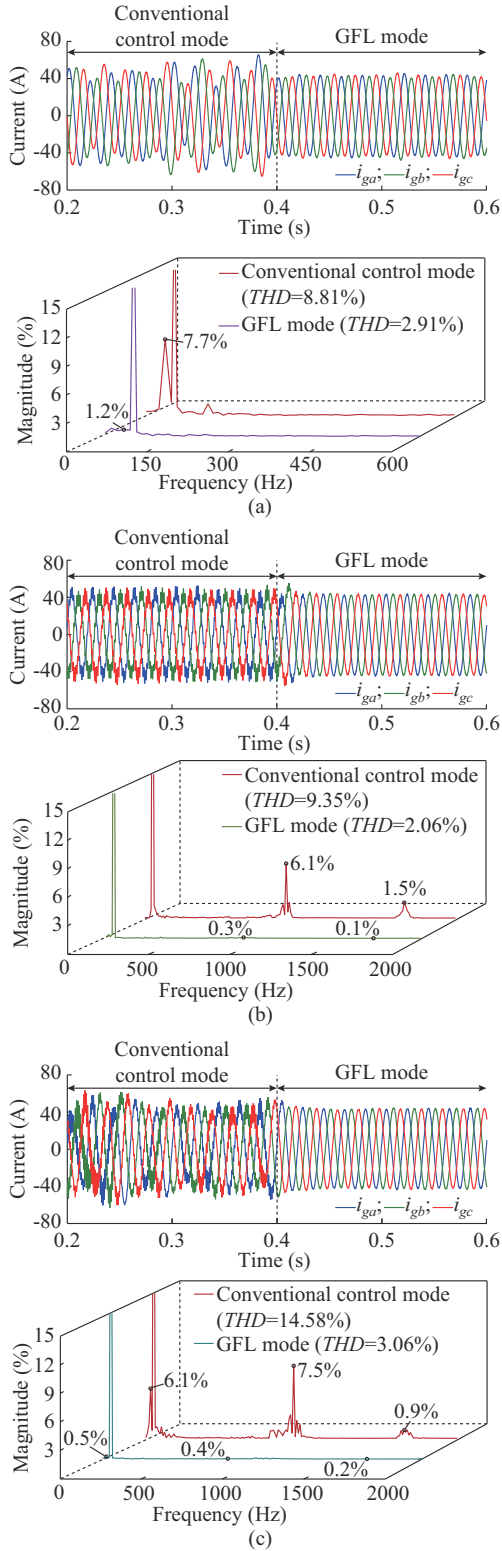


Fig. 16. Simulation verification of frequency-divided compensation strategy for HIPS. (a) GFL mode. (b) GFM mode. (c) Hybrid mode.

VI. CONCLUSION

This paper establishes the node admittance matrix of HIPS while fully considering PSL, PLL, and delay, thereby laying the foundation for analyzing the multi-dimensional oscillation characteristics of HIPS.

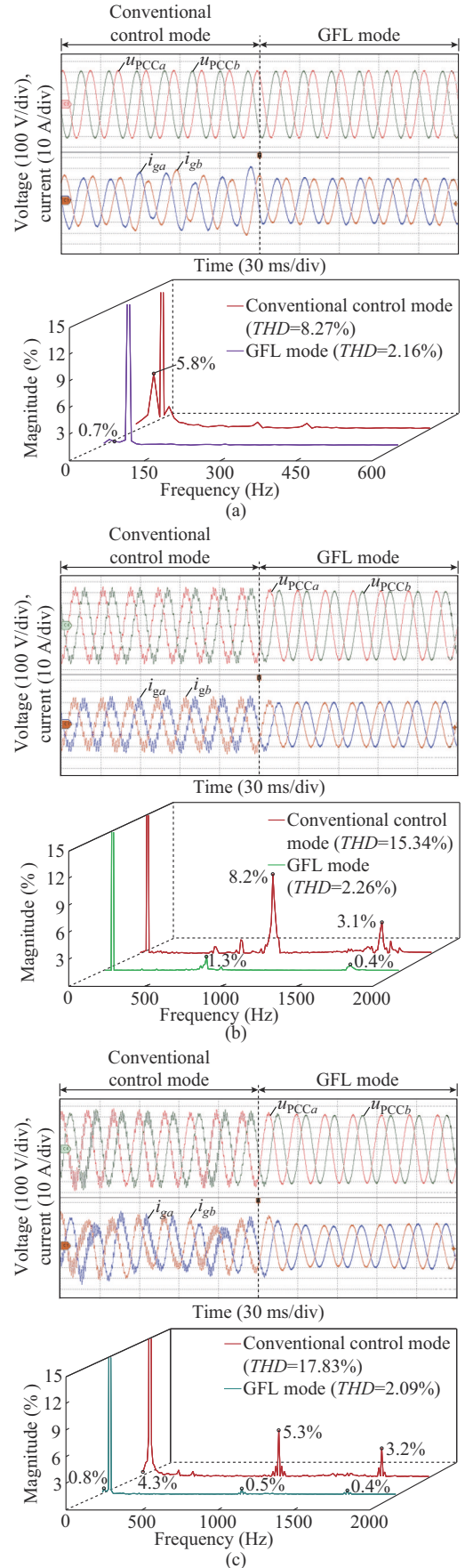


Fig. 17. Experimental verification of frequency-divided compensation strategy for HIPS. (a) GFL mode. (b) GFM mode. (c) Hybrid mode.

Additionally, the modal analysis and parameter sensitivity analysis are employed to identify the dominant factors of HIPS. Furthermore, a frequency-divided compensation strategy is proposed to address the multi-dimensional oscillation problem. The main conclusions are summarized as follows.

1) The constructed interactive admittance matrix model of HIPS fully considers the influences of both inverter and non-inverter factors, i.e., self-admittance and mutual admittance. This model maps the overall system stability to a single inverter, allowing the identification of the resonance-dominant inverter through the modal analysis results.

2) The primary reason for high-frequency oscillation destabilization of HIPS under weak grid conditions is the increased coupling of grid impedance with the current loop of GFL inverter. Conversely, the likelihood of low-frequency oscillation destabilization increases under strong grid conditions, with the voltage loop of GFM inverter being the dominant factor.

3) The frequency-divided compensation strategy classifies the operation modes of HIPS into three categories, i.e., GFM, GFL, and hybrid modes. This strategy dynamically adjusts the operation modes based on real-time harmonic power feedback, suppressing the oscillation frequency content to below 2% and ensuring the stable operation of HIPS.

Although the control strategy proposed in this paper effectively enhances stability, it has certain limitations.

1) GFM inverter is utilized solely as an example of droop control, and further consideration is needed for virtual synchronous generator control, virtual oscillator control, etc.

2) The integration of various types of electronic equipment introduces additional uncertainties to the HIPS, leading to complex fault types, which necessitates further improvements and expansions of the strategy based on actual engineering data.

These limitations provide a direction for future research to explore the potential for further optimizing the technology and advanced control algorithms by integrating the engineering applications with carbon emission reduction benefits.

APPENDIX A

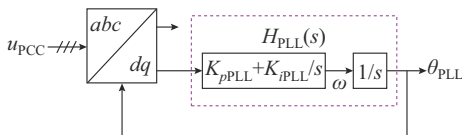


Fig. A1. Structural diagram of PLL based on synchronized coordinate system.

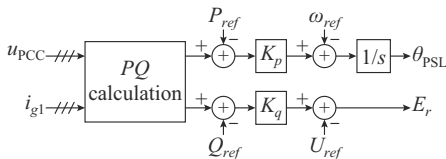


Fig. A2. Structural diagram of PSL based on droop control.

APPENDIX B

$$\begin{cases} T_p(s) = \frac{1}{2} \frac{H_{PLL}(s-j\omega_1)}{1 + V_0 H_{PLL}(s-j\omega_1)} G_d(s) \\ T_n(s) = \frac{1}{2} \frac{H_{PLL}(s+j\omega_1)}{1 + V_0 H_{PLL}(s+j\omega_1)} G_d(s) \end{cases} \quad (B1)$$

$$\begin{cases} Y_1(s) = 1 + \frac{L_{11} C_{\Pi} s^2}{C_{\Pi} s + 1} \\ Y_2(s) = sL_{11} + sL_{21} + \frac{L_{11} L_{21} C_{\Pi} s^3}{C_{\Pi} s + 1} \end{cases} \quad (B2)$$

$$C_p(s) = H_{vc}(s-j\omega_1) H_{vv}(s-j\omega_1) (jV_0 M(s-j\omega_1) + N(s-j\omega_1)) \cdot G_d(s) + (jE_r e^{j\theta_{v1}} + jI_0 e^{j\theta_{\phi}} H_{vc}(s-j\omega_1)) M(s-j\omega_1) \quad (B3)$$

$$C_n(s) = H_{vc}(s+j\omega_1) H_{vv}(s+j\omega_1) (jV_0 M(s+j\omega_1) - N(s+j\omega_1)) \cdot G_d(s) + (jE_r e^{j\theta_{v1}} + jI_0 e^{j\theta_{\phi}} H_{vc}(s+j\omega_1)) M(s+j\omega_1) \quad (B4)$$

$$\begin{cases} M(s) = -\frac{3k_p G_f(s)}{s} \\ N(s) = -\frac{3jG_f(s)}{k_q} \end{cases} \quad (B5)$$

$$G_f(s) \Big|_{\omega_c=50\pi} = \frac{\omega_c}{s + \omega_c} \quad (B6)$$

$$\begin{cases} Y_3(s) = 1 + \frac{L_{12} C_{\Pi} s^2}{C_{\Pi} s + 1} \\ Y_4(s) = sL_{12} + sL_{22} + \frac{L_{12} L_{22} C_{\Pi} s^3}{C_{\Pi} s + 1} \end{cases} \quad (B7)$$

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