Distributed Secondary Control Based on Dynamic Diffusion Algorithm for Current Sharing and Average Voltage Regulation in DC Microgrids

Dawei Liao, Fei Gao, Member, IEEE, Daniel J. Rogers, Senior Member, IEEE, Wentao Huang, Member, IEEE, Dong Liu, Senior Member, IEEE, and Houjun Tang

Abstract—This paper introduces a distributed secondary control scheme for achieving current sharing and average voltage regulation objectives in a DC microgrid. The proposed scheme employs a dynamic diffusion algorithm (DDA) instead of the consensus algorithm to enable distributed communication among converters. To help understand DDA, the relation of DDA and other diffusion algorithms is discussed in detail and its superiority is shown by comparison with diffusion and consensus algorithms. Furthermore, considering the discrete nature and different sampling time of the digital controller and communication network, a z-domain model of the entire DC microgrid is established. The influence of communication and secondary control parameters on the system stability is investigated. Based on the established model, the tolerable communication rates are obtained. Real-time simulations conducted on the OPAL-RT platform validate the effectiveness of the proposed scheme, showcasing its advantages in terms of convergence speed and stability.

Index Terms—Cooperative control, DC microgrid, diffusion algorithm, discrete-time modeling, distributed secondary control.

I. INTRODUCTION

MICROGRID (MG) provides a convenient and efficient interface to distributed energy resources (DERs), energy storage systems (ESSs), and loads [1]. Compared with AC MGs, because of the absence of frequency regulation, synchronization and reactive power management, DC MGs achieve higher efficiency with fewer conversion stages when faced with DC loads and ESSs. Considering these advantages, DC MGs are prevalently adopted in terrestrial applications and marine/aircraft onboard electrical systems [2], [3].

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A hierarchical control structure with three control layers is widely utilized in DC MGs [4], [5]. The primary control layer deals with local voltage/current regulation and power/current sharing. Within the primary control layer, droop control, a decentralized method that exclusively relies on local information and necessitates no communication links, is frequently employed to ensure proper load sharing among converters. On the other hand, the secondary control layer focuses on addressing voltage deviation caused by droop control and refining power sharing precision. This paper centers its attention on the primary and secondary control layers.

Secondary control strategies can be classified into centralized, distributed, and decentralized control strategies based on the communication means among converters [6]. In centralized control, a central controller is mandatory to collect global information and send commands to all converters. However, centralized control is susceptible to single points of failure (SPOFs). On the contrary, decentralized control is less complicated to deploy but may not deliver accurate load allocation or achieve MG global optimization [7]. As a result, distributed secondary control has been widely adopted in recent years. This strategy necessitates less communication investment since it only requires a sparse network to communicate among neighboring converters [8]. Additionally, it is impervious to communication breakdowns since there is no central controller [9].

Previous researches have tried implementing distributed control with various approaches [10] - [13]. Reference [11] achieves state of charge (SOC) balancing through DC bus signaling and a secondary controller for balancing. In [12], a hybrid method with both voltage-shifting and slope-adjusting approaches is proposed, which equalizes not only output currents but also output impedances of converters. Reference [13] proposes a current regulator that calculates the weighted average currents of all converters and a voltage regulator that shifts the droop curve to reach the accurate current sharing and suitable voltage regulation.

In recent years, secondary control strategies utilizing consensus algorithms have garnered significant attention and have been widely investigated. Several research studies have explored optimal design for communication networks [14], stability analysis taking into account communication delays [15], event-triggered mechanisms [16], distributed sliding

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mode controllers [17], and passivity-based controllers [18]. Additionally, various variants of consensus algorithms, such as proportional-integral (PI) consensus and ratio consensus, have been developed and implemented in secondary control [19]-[21]. While some studies have proposed fixed-time or finite-time structures to improve the convergence speed of the system [22]-[25], most of the related works still employ consensus algorithms.

Compared with consensus algorithm, diffusion algorithm is a superior solution to distributed estimation and adaptation problems [26]. Diffusion networks have faster convergence speed and reach lower mean-square deviation than consensus networks. However, its application in MGs is limited. In [27]-[29], the diffusion algorithm is applied in tertiary control layer to achieve optimal operation of an MG. Reference [30] applies an exact diffusion algorithm (EDA) in the tertiary control in MGs to achieve economic dispatch.

Besides tertiary control, the secondary control based on diffusion is also rarely studied. Reference [31] proposes a secondary control scheme based on the diffusion algorithm to achieve reactive power sharing and voltage control of an offshore wind farm. But the system stability is not analyzed. Reference [32] implements a secondary controller using a diffusion algorithm in an AC MG, of which the stability is analyzed via a continuous-time small-signal model. Reference [33] proposes a dynamic diffusion algorithm (DDA) based secondary control scheme in a single-bus DC MG, which requires the estimation of line impedances. In addition, the relation among different diffusion algorithms is not revealed in the aforementioned studies.

Additionally, there has been limited research on discretetime (DT) secondary control schemes, which are more practical than continuous-time ones. For instance, [34] develops a DT event-triggered secondary control scheme and proves its asymptotic convergence using Lyapunov synthesis. In [35], a DT secondary controller with a voltage observer using only the local current sharing errors is proposed. Although the upper bounds of control gains and constant power loads (CPLs) are obtained, the constraints on sampling periods are not quantitatively analyzed. In [36], a DT secondary control scheme based on the scheme in [37] is introduced. However, the upper bound of communication intervals is dependent on sampling values, and the dynamics of the inner control loops are neglected in the above studies. As the secondary control bandwidths increase rapidly due to the development of communication technology, the impact of the inner control loops becomes more significant.

To fill the aforementioned gaps, this paper proposes a distributed secondary control scheme using DDA in a multiplebus DC MG, of which the stability is analyzed in DT domain. The main contributions of this paper can be highlighted as follows.

1) The relation between DDA and other diffusion algorithms is fully demonstrated and the advantages of DDA is clearly shown.

2) A distributed secondary control scheme based on DDA is designed to achieve average voltage regulation and propor-

tional current sharing in a multiple-bus DC MG without knowledge of feeder line parameters. Convergence speed of the system is improved compared with finite-time controllers.

3) A detailed DT *z*-domain model of the whole system considering different sampling time of digital controllers and communication network is established, based on which the tolerable communication rate of the proposed scheme is obtained and is further validated via real-time simulations.

The rest of this paper is organized as follows. Section II shows the derivation of DDA and its superiority to traditional consensus and diffusion algorithms. Section III presents the proposed distributed secondary control scheme based on DDA. In Section IV, an overall DT model is described, based on which the stability analysis of overall system is conducted considering secondary control parameters and communication rate. Section V presents the real-time simulation results. Finally, Section VI concludes this paper.

II. DERIVATION OF DDA AND ITS SUPERIORITY TO TRADITIONAL CONSENSUS AND DIFFUSION ALGORITHMS

A diffusion algorithm was originally proposed for adaptive networks which consist of a collection of agents to solve an optimization problem in a distributed fashion [38]. In previous studies, the diffusion and its variants, EDA and DDA, are adopted in tertiary and secondary control layers [30], [33]. But the relation between them has not been mentioned yet. In order to better introduce DDA, this section shows the derivation of DDA as well as its superior performance over traditional consensus and diffusion algorithms.

A. Preliminaries on Graph Theory

The communication network among converters is denoted as a graph $\mathcal{G} = (\mathcal{V}, \mathcal{E}, \mathcal{A})$, where \mathcal{V} denotes the set of nodes, $\mathcal{E} \supseteq \mathcal{V} \times \mathcal{V}$ denotes the set of edges between two nodes, and $\mathcal{A} \in \mathbb{R}^{n \times n}$ denotes the adjacency matrix. Each node in the set $\mathcal{V} = \{1, 2, ..., n\}$ represents a distributed secondary controller and edges in the set \mathcal{E} represent the communication links among controllers. If there is a link between node *i* and node *j*, the two nodes are adjacent. Entities of the adjacency matrix \mathcal{A} are denoted as a_{ij} , which is defined by specific rules and network topology. If node *i* and node *j* are adjacent, $a_{ij} > 0$, and otherwise $a_{ij} = 0$. The set of neighboring nodes of node *i* is defined as $\mathcal{N}_i = \{j \in \mathcal{V} \mid (i, j) \in \mathcal{E}\}$.

B. Derivation of DDA

In the following derivation, an aggregated optimization problem of the form is considered:

$$\omega^* = \arg\min_{\omega \in \mathbb{R}} J(\omega) = \arg\min_{\omega \in \mathbb{R}} \sum_{i=1}^n J_i(\omega)$$
(1)

where $J_i(\omega)$ is the cost function of agent *i*, which is differentiable and convex; *n* is the number of agents; and ω is the decision variable. All agents seek to achieve the optimal solution ω^* only through communication with their neighbors.

To solve this problem, a diffusion algorithm is proposed in [39], which typically has an adapt-then-combine (ATC) structure as shown in (2).

$$\begin{cases} \psi_i(k) = \omega_i(k-1) - \mu \nabla J_i(\omega_i(k-1)) \\ \omega_i(k) = \sum_{j \in \mathcal{N}_i} a_{ij} \psi_j(k) \end{cases}$$
(2)

where $\omega_i(k)$ is the state of node *i* at sample *k*; $\psi_i(k)$ is the intermediate variable for node *i* at sample *k*; and μ is the coefficient of positive step size. Reference [26] has already shown that diffusion algorithm has a wider range of step sizes ensuring stability and a smaller spectral radius than consensus algorithms, which means that diffusion networks converge faster than consensus networks.

Nevertheless, this diffusion can be interpreted as the diagonally-weighted incremental iteration to solve the approximate penalized form of the original problem (1) [40]. When the step size is constant, the iteration in (2) converges towards a neighborhood of square-error size $O(\mu^2)$ around the optimal solution ω^* . Therefore, there always exists a bias between its solution and the real solution ω^* .

To remove the bias, a correction step is introduced into (2), and thus EDA proposed in [40] can be obtained as:

$$\begin{cases} \psi_i(k) = \omega_i(k-1) - \mu \nabla J_i(\omega_i(k-1)) \\ \phi_i(k) = \psi_i(k) + \omega_i(k-1) - \psi_i(k-1) \\ \omega_i(k) = \sum_{j \in \mathcal{N}_i} \bar{a}_{ij} \phi_j(k) \end{cases}$$
(3)

where $\phi_i(k)$ is the newly introduced intermediate variable; and \bar{a}_{ij} is the entity of the matrix $\bar{A} = (A + I_n)/2$, and I_n is the identity matrix.

In the secondary control, distributed algorithms are utilized in each controller to track a global average of a sampled value, such as output current and voltage. One way to achieve real-time tracking of the average of samples $\{r_i(k)\}$ is to consider the following cost function for each agent *i*:

$$J_i(\omega(k)) = \frac{1}{2} (\omega - r_i(k))^2$$
(4)

The corresponding aggregated cost function is expressed as:

$$J(\omega(k)) = \frac{1}{2} \sum_{i=1}^{n} (\omega - r_i(k))^2$$
(5)

The optimal solution of (5) will be exactly the average of the samples:

$$\omega^* = \frac{1}{n} \sum_{i=1}^n r_i(k) \tag{6}$$

Thus, by substituting (4) into (3), a variant of diffusion to track the average of samples, DDA, is obtained as [41]:

$$\begin{cases} \psi_i(k) = (1-\mu)\omega_i(k-1) + \mu r_i(k) \\ \phi_i(k) = \psi_i(k) + \omega_i(k-1) - \psi_i(k-1) \\ \omega_i(k) = \sum_{j \in \mathcal{N}_i} \bar{a}_{ij}\phi_j(k) \end{cases}$$
(7)

To guarantee stable convergence of the algorithm, the step size μ and the adjacency matrix A should satisfy certain conditions. The optimal choice of step size has not been reported by any research yet, but usually the step size should be limited within (0, 2], as discussed in Section IV.

A must be a right stochastic matrix, which means $A \cdot 1 = 1$ [40]. In addition, the network graph should be strongly connected, which means that there exists at least one path between any two nodes and at least one diagonal entry of A is non-zero. For the convenience of comparison, following the rules in [33] and [42], Metropolis rule [26] is adopted in this paper:

$$a_{ij} = \begin{cases} \frac{1}{\max\left\{n_i, n_j\right\}} & j \in \mathcal{N}_i, i \neq j \\ 1 - \sum_{j \in \mathcal{N}_i} a_{ij} & i = j \\ 0 & j \notin \mathcal{N}_i \end{cases}$$
(8)

where n_i is the number of neighbors of node *i*.

C. Comparison Between Consensus and Diffusion Algorithms

Consensus algorithm is commonly used to address distributed estimation problems in secondary control layer of MG. Thus, in this subsection, first, a dynamic consensus algorithm (DCA) with an optimal step size as described in [42] is used for comparison.

A six-node communication network with different topologies is tested to verify the convergence of DDA. The results of DDA are compared with those of DCA. Sampled values of each node r_1 - r_6 as well as communication rate of the network $T_{\rm com}$ are shown in Table I. The convergence of the network under different topologies is shown in Fig. 1.

TABLE I SAMPLED VALUES OF EACH NODE AND COMMUNICATION RATE OF A SIX-NODE COMMUNICATION NETWORK

Parameter	Value	Parameter	Value
r_1	1	r ₅	20
r_2	7	r ₆	25
r_3	13	T _{com}	10 ms
r_4	18		

As shown in Fig. 1, both DDA and DCA can converge under all four topologies. Among the four topologies, cross topology brings the fastest convergence speed and line topology shows the slowest convergence speed. Compared with the results of DCA, DDA has nearly the same performance under line topology and shows faster convergence speed under star, ring, and cross topologies.

Additionally, the convergence performance of DDA is compared with that of traditional diffusion algorithm. The mean square error (MSE) is defined below to quantify the convergence performance:

$$MSE = \frac{1}{n} \sum_{i=1}^{n} (\omega_i - r_i)^2$$
(9)

Equation (9) represents the convergence accuracy at the steady state. A lower MSE implies a more accurate convergence. The MSEs of traditional diffusion algorithm and DDA under a step size of 0.1 after 10000 iterations are compared in Table II. It is observed that DDA can reach a much lower MSE than traditional diffusion algorithms.

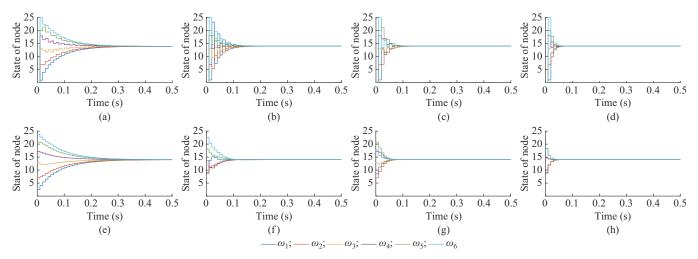


Fig. 1. Convergence of a six-node communication network under different topologies using DCA and DDA. (a) DCA under line topology. (b) DCA under star topology. (c) DCA under ring topology. (d) DCA under cross topology. (e) DDA under line topology. (f) DDA under star topology. (g) DDA under ring topology. (h) DDA under cross topology.

TABLE II MSE OF TRADITIONAL DIFFUSION ALGORITHM AND DDA

	MSE			
Algorithm	Line topology	Ring topology	Cross topology	Full topology
Diffusion	1.394×10^{0}	2.742×10 ⁻²	1.077×10^{-3}	1.093×10^{-3}
DDA	5.762×10^{-27}	3.029×10^{-28}	1.396×10 ⁻²⁰	2.346×10^{-24}

III. PROPOSED DISTRIBUTED SECONDARY CONTROL BASED ON DDA

Figure 2 shows the DC MG with multiple buses under study. Distributed generators (DGs) are connected to local buses through LC filters, delivering power to loads that locate at different DC buses. Each converter is equipped with a hierarchical controller that contains two layers: distributed secondary control and local primary control. While the local primary control regulates output voltage and current, the distributed secondary control communicates with neighbors through communication links and sends a correction signal to the local primary control layer to achieve cooperative control among converters. It is worth noting that the communication network does not need to have the same topology as the electrical network.

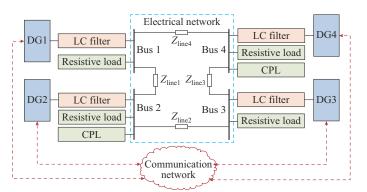


Fig. 2. Layout of a DC MG with multiple buses.

A. Local Primary Control Based on Droop Control

The local primary control layer incorporates three cascaded control parts, which are droop control, primary voltage PI control, and primary current PI control. In this control layer, only local measurements are required. In the droop control, as shown in Fig. 3, a virtual output resistance R_{di} is introduced:

$$v_{\text{ref},i}^{\text{droop}} = V^* - R_{di} i_{L,i} \tag{10}$$

where $v_{\text{ref},i}^{\text{droop}}$ is the reference voltage given by droop control; V^* is the nominal bus voltage; and $i_{L,i}$ is the inductor current of converter *i*.

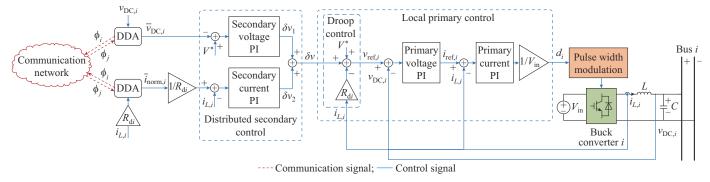


Fig. 3. Proposed distributed secondary control scheme.

If the primary voltage PI control and current PI control are properly designed, the bus voltage can track the reference given by the droop control:

$$v_{\text{DC},i} = v_{\text{ref},i}^{\text{droop}} \tag{11}$$

where $v_{DC,i}$ is the bus voltage of DG *i*. Thus, bus voltages deviate from the nominal value V^* with only the local primary control based on droop control applied.

Moreover, the current sharing performance is dependent on the electrical network besides virtual resistances. At steady state, the output currents of converters are equal to inductor currents:

$$i_{\mathrm{DC},i} = i_{L,i} \tag{12}$$

where $i_{DC,i}$ is the output current of converter *i*, which is also the current injection to bus *i*. Considering (10)-(12), due to the line impedances between buses, bus voltages differ from each other, resulting in unequal current sharing.

Therefore, to address the voltage deviation and current sharing problems, the droop curve is shifted by a correction term δv :

$$v_{\text{ref},i} = V^* - R_{di}i_{L,i} + \delta v \tag{13}$$

where $v_{\text{ref},i}$ is the reference voltage given by droop control and secondary control.

The cascaded inner control loops are expressed as:

$$i_{\text{ref},i} = G_{\text{V}}(s) \left(v_{\text{ref},i} - v_{\text{DC},i} \right)$$
 (14)

$$d_{i} = \frac{1}{V_{in}} G_{\rm C}(s) \left(i_{{\rm ref},i} - i_{L,i} \right)$$
(15)

where $i_{\text{ref},i}$ is the reference current; d_i is the duty cycle; V_{in} is the input voltage of the converter; and $G_V(s)$ and $G_C(s)$ represent the primary voltage PI control and current PI control, respectively.

B. Proposed Distributed Secondary Control

Secondary control aims to eliminate the voltage deviation and improve the current sharing accuracy. First, the average voltage of all buses is supposed to be the nominal voltage V^* :

$$\frac{1}{\eta} \sum_{i=1}^{n} v_{\text{DC},i} = V^*$$
(16)

Second, the currents should be allocated according to the droop gains (17), which is equivalent to (18).

$$\frac{i_{L,i}}{i_{L,j}} = \frac{R_{dj}}{R_{di}}$$
(17)

$$i_{\text{norm},i} = R_{di} i_{L,i} = R_{dj} i_{L,j} = i_{\text{norm},j}$$
 (18)

where $i_{\text{norm},i}$ is the normalized current.

The proposed distributed secondary control scheme is shown in Fig. 3. Two aforementioned objectives are achieved by the secondary voltage PI control and secondary current PI control, respectively, which generate two correction terms, δv_1 and δv_2 :

$$\delta v_1 = \left(K_{\text{psv}} + \frac{K_{\text{isv}}}{s} \right) \left(V^* - \bar{v}_{\text{DC},i} \right)$$
(19)

$$\delta v_2 = \left(K_{\rm psc} + \frac{K_{\rm isc}}{s} \right) \left(\frac{\bar{i}_{\rm norm,i}}{R_{\rm di}} - i_{L,i} \right) \tag{20}$$

where K_{psv} and K_{isv} are the proportional and integral gains for the secondary voltage PI control, respectively; K_{psc} and K_{isc} are the proportional and integral gains for the secondary current PI control, respectively; $\bar{v}_{DC,i}$ is the average bus voltage estimated locally at converter *i*; and $\bar{i}_{norm,i}$ is the average normalized current estimated locally at converter *i*. The two terms are combined to shift the droop curve:

$$\delta v = \delta v_1 + \delta v_2 \tag{21}$$

The references for the secondary voltage PI control and secondary current PI control include $\bar{v}_{DC,i}$ and $\bar{i}_{norm,i}$. In the proposed scheme, secondary controllers estimate the average values through a sparse communication network using DDA. For average voltage estimation, the observation variable $r_i(k)$ in DDA is replaced by the locally measured output voltage $v_{DC,i}$ and the state variable $\omega_i(k)$ is the locally estimated average voltage $\bar{v}_{DC,i}$. For average normalized current estimation, the observation variable $r_i(k)$ in DDA is the local normalized current $i_{norm,i}$ and the state variable $\omega_i(k)$ is the locally estimated average normalized current $\bar{i}_{norm,i}$.

Remark 1: according to (13) and (21), if the local primary control can well follow the reference voltage, the average voltage can be expressed as:

$$\bar{v} = \frac{1}{n} \sum_{i=1}^{n} \left(V^* - R_{di} i_{L,i} + \delta v_{1,i} + \delta v_{2,i} \right) = \frac{1}{n} \sum_{i=1}^{n} V^* + \frac{1}{n} \sum_{i=1}^{n} \left(-R_{di} i_{L,i} + \delta v_{1,i} \right) + \frac{1}{n} \sum_{i=1}^{n} \delta v_{2,i} \quad (22)$$

When the gains in the secondary current PI control are the same, the sum of $\delta v_{2,i}$ satisfies:

$$\sum_{i=1}^{n} \delta v_{2,i} = \left(K_{\text{psc}} + \frac{K_{\text{isc}}}{s} \right) \sum_{i=1}^{n} \left(\frac{\bar{i}_{\text{norm},i}}{R_{\text{d}i}} - i_{L,i} \right) = 0$$
(23)

Thus, the outputs of secondary current PI control impose no effect on the average voltage. Therefore, while determining the secondary control gains, K_{psv} and K_{isv} in secondary voltage PI control can be considered first regardless of secondary current PI control. After a desired response of average voltage restoration is achieved, secondary current PI control gains can be designed to improve current sharing performance.

Remark 2: when designing a traditional dual-loop controller or other local cascaded controllers, one of the important considerations is that upper controllers should be decoupled from the lower controllers. In distributed secondary control, control parameters should also be designed according to the communication conditions, which will be further illustrated in Section IV.

IV. STABILITY ANALYSIS

In this section, a DT z-domain model of the MG is established, based on which the system stability is investigated under different control parameters. Moreover, the tolerable communication rate based on DDA is compared with that of DCA. The specifications of the MG as well as parameters of loads and tie-lines are listed in Tables III and IV. R_{line} , L_{line} , $R_{\text{load}i}$, and $P_{\text{CPL}i}$ (i = 1, 2, 3, 4) are the line resistances, line inductances, load resistances, and power of constant power loads, respectively. The communication network is of ring topology.

TABLE III ELECTRICAL AND CONTROL PARAMETERS OF MG

Category	Parameter	Value
Input voltage	$V_{\rm in}$	100 V
LC filter	L	1.8 mH
LC Inter	С	2200 µF
Droop gain	R_{di}	0.1
Drimory voltogo DI	$K_{_{\mathrm{pv}}}$	13
Primary voltage PI	K_{iv}	800
Deimenen erwent DI	$K_{\rm pc}$	5
Primary current PI	, K _{ic}	100
	K _{psv}	0.02
Secondary voltage PI	K _{isv}	23
Concern dama array of DI	$K_{\rm psc}$	0.1
Secondary current PI	$K_{ m isc}$	5.5
Controller sampling time	T _d	100 µs
Communication rate of network	$T_{\rm com}$	10 ms
Step size	μ	0.5

TABLE IV PARAMETERS OF LOADS AND TIE-LINES

Parameter	Value
$R_{\text{line1}}, R_{\text{line2}}, R_{\text{line3}}, R_{\text{line4}}$	0.1, 0.12, 0.1, 0.13 Ω
$L_{\text{line1}}, L_{\text{line2}}, L_{\text{line3}}, L_{\text{line4}}$	0.011, 0.013, 0.011, 0.016 mH
$R_{\text{load1}}, R_{\text{load2}}, R_{\text{load3}}, R_{\text{load4}}$	13, 9, 8, 7 Ω
$P_{\text{CPL1}}, P_{\text{CPL2}}, P_{\text{CPL3}}, P_{\text{CPL4}}$	0, 300, 0, 300 W

A. DT System Modeling

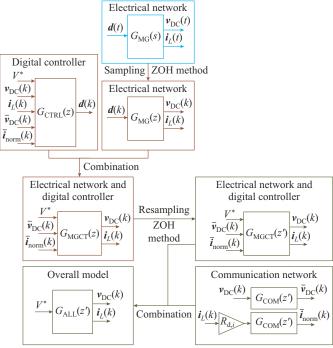
The modeling method in [42] is employed in order to consider the DT feature of communication networks and different sampling time of electrical networks, digital controllers, and communication networks. These three parts of the system, i.e., the electrical network, the digital controllers, and the communication network, are modeled separately first and then discretized, resampled, and integrated into an overall model. The overall model encompasses a z-domain communication network model with a sampling time of $T_{\rm com}$, a z-domain controller model with a sampling time of T_d , and an sdomain main circuit model. The integration process of the overall model is depicted in Fig. 4 in detail. d, $v_{\rm DC}$, i_L , $\bar{v}_{\rm DC}$, \overline{i}_{norm} are the vector variables of duty cycles, output voltages, inductor currents, normalized output voltages, and normalized currents, respectively; G_{MG} , G_{CTRL} , and G_{COM} are the transfer functions of the electrical network model, the digital controller, and the communication network, respectively; G_{MGCT} is the transfer function of the combined model of the electrical network and the digital controller; and $G_{\rm ALL}$ is the transfer function of the overall model.

Step 1: the continuous-time main circuit model is discretized using zero-order-hold (ZOH) method with a sampling time of T_{d} .

Step 2: the DT controller model and main circuit model are combined by connecting the corresponding input/output ports.

Step 3: the model is resampled with a sampling time $T_{\rm com}$.

Step 4: by connecting the corresponding input/output ports, the model in *Step 3* is combined with the communication network model to obtain the overall model.



 \Box Continuous time; \Box DT (sampling time T_d); \Box DT (sampling time T_{com})

Fig. 4. Integration process of overall model containing electrical network, digital controllers, and communication network.

B. Observation Weight μ

The observation weight μ in DDA is an important parameter that affects system stability. The root contour of the overall model with μ changing from 0.5 to 2.1 with a step of 0.2 is shown in Fig. 5. *T* is the sampling period. The blue stars represent the poles of the system. The arrows show the changing direction of poles as the parameter changes. It can be observed that when the observation weight is between 0.5 and 1.9, the system stability is guaranteed.

C. Integral Gain of Secondary Voltage Controller K_{isv}

Secondary PI controllers also affect the cooperated control among converters. The root contour with the integral gain of secondary voltage PI controller K_{isv} changing from 50 to 750 with a step of 25 is shown in Fig. 6. The upper bound of K_{isv} ensuring the system stability is 700.

D. Communication Rate $T_{\rm com}$

Communication rate $T_{\rm com}$ also influences the stability of cooperative strategies [43]. The root contour with communication rate changing from 30 to 300 ms with a step of 30 ms is shown in Fig. 7. The upper bound of communication rate is 300 ms according to the root contour, indicating that an increasing $T_{\rm com}$ deteriorates the communication conditions among converters and finally results in instability.

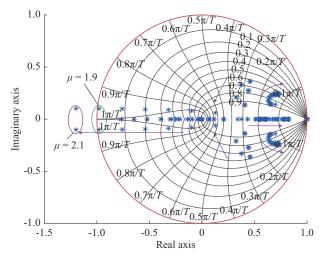


Fig. 5. Root contour with μ changing from 0.5 to 2.1 with a step of 0.2.

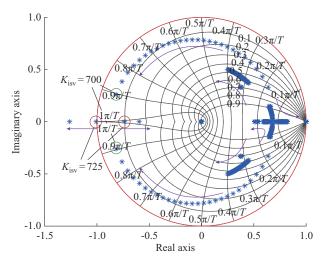


Fig. 6. Root contour with K_{isv} changing from 50 to 750 with a step of 25.

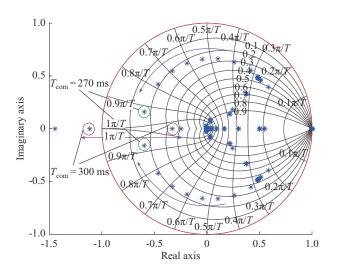


Fig. 7. Root contour with $T_{\rm com}$ changing from 30 to 300 ms with a step of 30 ms.

In the following, the upper bounds of tolerable communication rate $T_{\rm com}$ under different gains $K_{\rm psv}$ and $K_{\rm isv}$ are obtained using an exhaustive method. $K_{\rm psv}$ is set to be a value between 1 and 2 with a step of 0.05 and $K_{\rm isv}$ is set to be a value between 50 and 250 with a step of 25. Under each set of gains, the communication rate changes from 1 to 100 ms with a step of 1 ms. The largest communication rate that maintains the system stability is regarded as the upper bounds under the corresponding controller gains.

The tolerable communication rate of the proposed scheme under different K_{psv} and K_{isv} is compared with that of the DCA-based scheme in Fig. 8. As can be observed from Fig. 8, generally, when K_{psv} and K_{isv} become larger, the tolerable communication rate becomes lower. Compared with the DCA-based scheme, the proposed scheme guarantees the system stability at a lower communication rate.

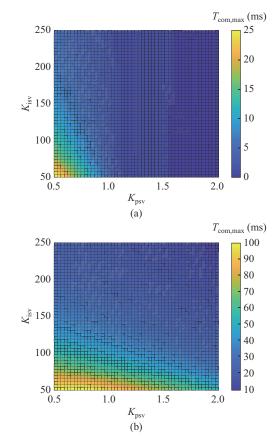


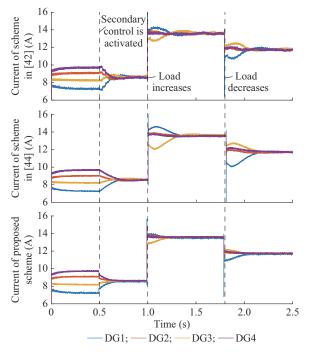
Fig. 8. Tolerable communication rate of proposed scheme and DCA-based scheme. (a) Proposed scheme. (b) DCA-based scheme.

V. RESULTS AND DISCUSSIONS

The proposed scheme is tested through real-time simulations with 1-µs step size on OPAL-RT OP 5700 platform as depicted in Appendix A Fig. A1. The tested MG is shown in Fig. 2. Specifications of the MG are listed in Table III. Tieline and load parameters are listed in Table IV.

A. Performance Evaluation

In this case, the performance of the proposed scheme is tested under load change to illustrate its effectiveness. The secondary control is activated at 0.5 s. The CPLs on bus 2 and bus 4 increase from 300 W to 600 W at 1.0 s. The resistive load on bus 1 changes from 13 Ω to 4.3 Ω at 1.0 s and back to 13 Ω at 1.8 s. The dynamic performance of the proposed scheme is compared with that of the DCA-based



scheme in [42] and the unified secondary control scheme in [44], as shown in Figs. 9 and 10.

Fig. 9. Current sharing performance of different schemes.

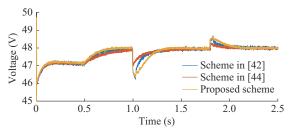


Fig. 10. Comparison of average voltage restoration performances.

After 0.5 s, under the proposed scheme, the output currents are accurately shared. When the load increases at 1.0 s and decreases at 1.7 s, accurate current sharing is still guaranteed by the secondary control. Also, as Fig. 11 shows, after secondary control is activated, the average bus voltage is regulated to the nominal value and is maintained regardless of load changes at 1.0 s and 1.8 s. The performance indexes such as the convergence time and overshoots are listed in Table V. Although the overshoot of the average voltage is larger, the proposed scheme has a faster convergence speed. The comparison with the schemes in [42] and [44] validates the advantage of the proposed scheme.

 TABLE V

 PERFORMANCE INDEXES OF SCHEMES IN [42], [44] AND PROPOSED SCHEME

Scheme	Convergence time for current sharing (s)	Convergence time for voltage regulation (s)	Overshoot of average voltage (V)
[42]	0.4	0.9	1.8
[44]	0.4	0.5	1.0
Proposed	0.2	0.3	1.8

Furthermore, the communication rate and convergence speed of the proposed scheme are also compared with those of the fixed-time/finite-time schemes, as shown in Table VI. It is shown that the proposed scheme reduces the convergence time in similar communication conditions.

TABLE VI Comparison of Communication Rate and Convergence Speed of Proposed Scheme and Fixed-time/Finite-time Schemes

Scheme	Communication rate (ms)	Convergence time (s)
[22]	29	0.50
[23]	20	0.25
[24]	6	0.30
[25]	10	0.26
Proposed	10	0.20

B. Effect of Communication Rate

In practice, cooperative control schemes are usually faced with different intricacies in cyber systems, such as delay and data packet loss. Packet loss and other communication failures are often addressed by retransmission, which, as well as delay, decreases effective communication rate in essence. Therefore, system performance of the proposed scheme under different communication rates is also evaluated and shown in Fig. 11. Besides, system performance of different schemes with $T_{\rm com} = 100$ ms is shown in Fig. 12. The MG undergoes the same load changes as mentioned above.

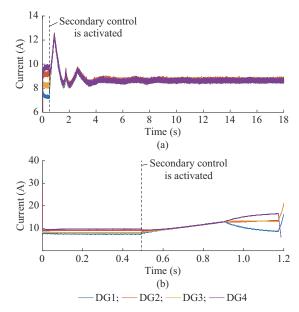


Fig. 11. Current sharing performance of proposed scheme under different communication rates. (a) $T_{\rm com} = 260$ ms. (b) $T_{\rm com} = 300$ ms.

It is shown in Fig. 11 that when T_{com} reaches 260 ms, the system is still stable, while a communication rate of 300 ms destabilizes the system, which is consistent with eigenvalue analysis as shown in Fig. 7.

It is observed in Fig. 12(a) that when $T_{com}=100$ ms, which indicates a lower communication rate, the settling time becomes longer. Nevertheless, desired current sharing and volt-

age restoration are still achieved. Correspondingly, the system is already unstable under the DCA-based scheme when $T_{\rm com}$ increases to 100 ms, as shown in Fig. 12(b).

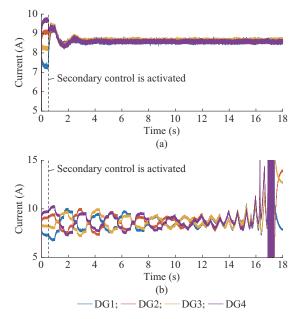


Fig. 12. Current sharing performance with $T_{\rm com} = 100$ ms. (a) Proposed scheme. (b) DCA-based scheme.

C. Communication Link Failure

System resiliency to communication link failure is also studied. The secondary control is activated at 0.5 s. The CPLs on bus 2 and bus 4 increase from 300 W to 600 W at 0.8 s. The resistive load changes from 13 Ω to 4.3 Ω at 1.5 s and back to 13 Ω at 2.5 s. The communication link between converters 1 and 4 abruptly fails at 1.0 s and recovers at 2.6 s.

As shown in Fig. 13(a), before the failure at 0.6 s, it takes 0.2 s for currents to converge. Accurate current sharing is still achieved under the communication link failure and the load change afterwards. Although the broken link is recovered at 1.5 s in the middle of converging process, the system stability is guaranteed and currents can still converge within 0.3 s. Also, the average voltage is well maintained during the link failure as well as during the load changes, as shown in Fig. 13(b).

D. Converter Fault

In this case, system resiliency to a converter fault is studied. The secondary control is activated at 0.5 s. Converter 3 breaks down at 1.5 s and recovers at 2.5 s. A change in communication network comes along with the fault. As shown in Fig. 14(a), after converter 3 breaks down, the other 3 converters are still able to share the load current proportionately. After converter 3 recovers, its output current restores from 0 and reaches a consensus with the output currents of the other 3 converters. Besides, Fig. 14(b) shows that the average bus voltage of converters is well maintained at 48 V regardless of converter fault.

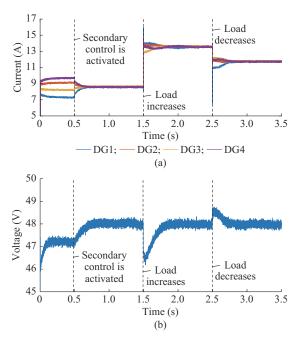


Fig. 13. System performance under communication link failure. (a) Current sharing. (b) Average voltage regulation.

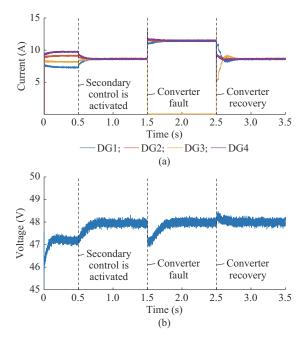


Fig. 14. System performance under converter fault. (a) Current sharing. (b) Average voltage regulation.

E. Discussion

As shown by the stability analysis and real-time simulation results, the proposed scheme reduces the convergence time and enhances the stability in the presence of high communication rates. However, when the number of converters increases, the communication process time also increases, leading to higher communication rates. Thus, the proposed scheme offers advantages in scenarios involving numerous converters.

However, the addition of a correction step to DDA, as presented in (3), increases its intermediate variables to 2n. In comparison, DCA and traditional diffusion algorithms contain only n intermediate variables. As the number of converters increases, the computational and memory requirements of the proposed scheme may also increase.

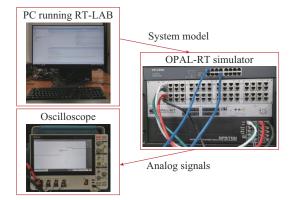
VI. CONCLUSION

This paper proposes a distributed secondary control scheme based on DDA to achieve average voltage restoration and proportional current sharing in DC MGs by sending a voltage shifting signal to the primary control. Main findings of this paper can be summarized as follows.

1) A dynamic variant of diffusion, DDA, is introduced and the proposed distributed secondary control scheme using DDA can achieve accurate current sharing among converters and bus voltage regulation in a distributed way. The proposed scheme is shown to have faster convergence speed than conventional control scheme utilizing consensus algorithm.

2) Based on an overall DT model of a DC MG, a specific range of communication and control parameters ensuring system stability is obtained according to root contours.

3) The tolerable communication rates under different control parameters are approximated using exhaustion method verifying that the MG using the proposed scheme is able to keep stable under a lower communication rate than the conventional DCA-based scheme.



APPENDIX A

Fig. A1. Real-time simulation platform setup.

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